

Random doping induced fluctuations in p-n junction diodes

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Abstract: - Random doping induced fluctuations in p-n junction diodes are analyzed by using the linearization technique. The doping concentration is considered a random variable in the transport equations and all fluctuating quantities are linearized around their average values. Numerical results for a p-n junction diode with simplified structure are presented for the fluctuations of the terminal currents and small-signal parameters.

Key-Words: - linearization technique, sensitivity analysis, random doping induced fluctuations.

1 Introduction

Dopant concentrations in semiconductor devices [1] are subject to stochastic variations due to the random nature of ion implantation and diffusion processes. Random fluctuations of doping are especially pronounced in small devices where spatial scales of these fluctuations are more or less comparable with device dimensions. The random dopant fluctuations lead to appreciable fluctuations of device parameters. For this reason, an accurate analysis of random dopant-induced effects is very important for further progress in the area of semiconductor device technology.

There are two conceptually different approaches to the analysis of random doping induced fluctuations in semiconductor devices. The first approach [2], known as the Monte Carlo approach, is based on generating numerous realizations of the doping and solving the transport equations for each of such realization. Statistics of different parameters of interest are then accumulated and used to evaluate the average values and variances of those parameters. These methods are computationally very expensive since the same device-level simulations have to be performed many times. The second approach [3]-[5], known as the linearization technique is based on linearization of the transport equations with respect to the fluctuating quantities and allows the computation of the standard deviation of intrinsic parameters of semiconductor devices, such as terminal currents and frequency characteristics. The linearization technique is much more computationally efficient than the Monte Carlo method because it circumvents computations for many devices. In addition, the linearization technique provides information on the sensitivity of parameter variances to locations of the random

doping fluctuations, which makes it instrumental in the design of fluctuation resistant structures. In this article we develop a linearization technique for the analysis of fluctuations of terminal currents and small-signal parameters in p-n junction diodes.

The article is structure as follows. The transport model used in the simulations is briefly presented in Section II. In Section III we present the linearization technique and we apply it to the analysis of fluctuations of terminal currents and small-signal parameters. Numerical simulations results are presented in Section IV, which is followed by Conclusions.

2 Transport model

The electron and hole concentrations in p-n junctions can be described by using the classical drift-diffusion model [6]:

$$\nabla \cdot (\epsilon \nabla \varphi) + q(p - n + D) = 0, \quad (1)$$

$$\frac{\partial n}{\partial t} = \nabla \cdot (-\mu_n n \nabla \varphi + D_n \nabla n) - R(n, p, \varphi), \quad (2)$$

$$\frac{\partial p}{\partial t} = \nabla \cdot (\mu_p p \nabla \varphi + D_p \nabla p) - R(\varphi, n, p), \quad (3)$$

where R is the electron-hole recombination rate, φ is the electric potential, and n , μ_n , D_n and p , μ_p , D_p are the concentration, mobility, and diffusivity of the electrons and holes, respectively. In our simulations the electron and hole mobilities are described by the well-calibrated model presented in [4] and [5].

3 Analysis of fluctuations by linearization

In general, any parameter A such as terminal currents or small-signal parameters can be written as a function of doping concentration D in the diode [7]:

$$A = A(D). \tag{4}$$

In equation (4), we have considered that the doping concentration is a function of position. Hence, if the region of the semiconductor device is spatially discretized into N cells, the doping concentration must be specified as an N -dimensional column vector whose components are the mesh-point values of the doping concentration. The doping concentration vector is as a random quantity, which can be written as the sum of its average value D_0 and fluctuation \mathcal{D}^0 :

$$D = D_0 + \mathcal{D}^0, \tag{5}$$

where by definition, the expected value of \mathcal{D}^0 is equal to zero. In the first-order approximation, the fluctuations of parameter A can be computed by linearization:

$$\mathcal{A}^0 = \frac{\partial A}{\partial D} \mathcal{D}^0 = \sum_i \gamma_A^i \mathcal{D}_i^0, \tag{6}$$

where γ_A^i are called the doping superposition coefficients. These coefficients show how sensitive parameter A is to the fluctuations of doping concentration at specific locations in the device. It is customary to assume that doping densities at different locations are independent random variables. This allows us to derive the following expression for the variance of parameter A :

$$\sigma_A^2 = \sum_i (\gamma_A^i)^2 \sigma_{D_i}^2, \tag{7}$$

where $\sigma_{D_i}^2$ represents the variance of D_i . To determine $\sigma_{D_i}^2$, we consider that the total number N_i of dopant atoms in the i th discretization cell of volume ΔV_i is a random variable with Poisson distribution. Hence, we have $\sigma_{N_i}^2 = N_{0i} = D_{0i} \Delta V_i$, where, D_{0i} is the average doping concentration in volume ΔV_i . By using this fact, we derive:

$$\sigma_{D_i}^2 = \left(\frac{1}{\Delta V_i} \right)^2 \sigma_{N_i}^2 = \frac{D_{0i}}{\Delta V_i}. \tag{8}$$

By using equations (7) and (8), the variance of parameter A can be computed as follows:

$$\sigma_A^2 = \sum_i (\gamma_A^i)^2 \frac{D_{0i}}{\Delta V_i}. \tag{9}$$

Thus, the problem of computing the variance of parameter A is reduced to the computation of superposition coefficients.

It should be noted that the superposition coefficients are mesh dependent. Therefore, it is sometimes convenient to introduce the doping “sensitivity coefficients,” S_A^i , which are defined as [3]:

$$S_A^i = \left(\frac{\gamma_A^i}{\Delta V_i} \right)^2. \tag{10}$$

With notation (10), equation (9) reads as follows:

$$\sigma_A^2 = \sum_i (S_A^i)^2 D_{0i} \Delta V_i. \tag{11}$$

3.1 Superposition coefficients of the current

The analysis of current fluctuations is extremely important for both digital and analog applications. For the sake of brevity we will consider that $\frac{\partial n}{\partial t} = \frac{\partial p}{\partial t} = 0$ (steady state) and write the transport equations (1)-(3) in discretized form [8]:

$$F(X, D) = 0, \tag{12}$$

where F is a nonlinear vector function of the unknown “state” vector X and doping vector D . The state vector consist of the mesh-point values of the electrostatic potential ϕ , electron and hole concentration n , and p , and: $X = [\phi \ n \ p]^t$, where t denoted the transpose of the given vector. Fluctuations of the doping concentration \mathcal{D}^0 induce fluctuations in the state vector \mathcal{X}^0 that can be computed by solving the following linear system:

$$\hat{F}_X \mathcal{X}^0 + \hat{F}_D \mathcal{D}^0 = 0, \tag{13}$$

where \hat{F}_X and \hat{F}_D are the derivatives of F with respect to X and D , respectively. These derivatives are computed at the given dc bias point and by assuming constant (non-fluctuating) values of the doping concentration.

Now, let us denote the current through the diode by I . In order to compute the superposition coefficients of I it is convenient to write I as a explicit functions of state vector X and doping concentration D :

$$I_{Diode} = I(X, D). \tag{14}$$

The fluctuations of terminal current can be found by linearizing (14) with respect to the fluctuating quantities:

$$\mathcal{I}^0 = (I_X)^t \mathcal{X}^0 + (I_D)^t \mathcal{D}^0, \tag{15}$$

where I_X and I_D are the derivatives of I with

respect to the state variable and doping concentration. By solving equations (13) and (15) for the fluctuations of the terminal current, one gets:

$$i_{\alpha} = - \left[\mathbf{g}^t \cdot \hat{\mathbf{F}}_D - (I_D)^t \right] \cdot \mathbf{D}_0, \quad (16)$$

where \mathbf{g}^t is the transpose of column vector \mathbf{g} , which is the solution of the following linear system of equations:

$$\hat{\mathbf{F}}_X^t \mathbf{g} = I_X, \quad (17)$$

where $\hat{\mathbf{F}}_X^t$ denotes the transpose of matrix $\hat{\mathbf{F}}_X$. By comparing (6) and (16), it can be inferred that the superposition coefficients of the terminal current are given by the following formula:

$$\gamma_{I_{\alpha}}^i = - \left(\mathbf{g}^t \cdot \hat{\mathbf{F}}_D \right)_i + (I_D)_i^t. \quad (18)$$

The standard deviation of terminal currents can be calculated now by using: $\sigma_i^2 = \sum_i (\gamma_i^i)^2 \frac{D_{0i}}{\Delta V_i}$. The

most expensive computational task related to the calculation of these coefficients is to solve linear system (17). For 2-dimensional simulations this system can be solved numerically by using the classical Gauss-Seidel method. However, for 3-dimensional simulations the computation time and memory requirements increase considerably if the Gauss-Seidel method is used, so other procedures are needed to solve (17). In our simulations, we took advantage of the diagonal dominance of matrix $\hat{\mathbf{F}}_X$ and solved it efficiently by using the Successive Over-Relaxation (SOR) method.

3.2 Superposition coefficients of the small-signal parameters

In this section we introduce the technique for the calculation of variances of admittance matrix elements (i.e. variances of y-parameters). We suppose that one of the terminals of the diode is grounded, while a small-signal ac potential v is applied on the other terminal. The admittance is defined as:

$$y = \frac{i}{v}, \quad (19)$$

where i small-signal phasor current through the terminals of the diode. The variance of parameter y can be computed in three steps:

1. First, we solve the transport equations and find the dc bias point.
2. Then, we solve the linearized transport equations and find the values of y-parameters.
3. Finally, we find the superposition coefficients and the variances of y-parameters.

Each of these steps is discussed below.

3.2.1 Finding dc bias point

The first step in the evaluation of the variance of y-parameter is to solve the transport equations and to find the dc bias point of the device. To this end, transport equations (1)-(3) can be written in discretized form as follows:

$$\frac{d}{dt} \mathbf{J}(\mathbf{X}) + \mathbf{F}(\mathbf{X}, \mathbf{D}, V) = 0, \quad (20)$$

where V is the voltage across the diode and \mathbf{J} is a vector function which depends on \mathbf{X} only. In equation (20), we have separated the explicit time-dependent part of the transport equations (which comes from the terms $\partial n / \partial t$ and $\partial p / \partial t$ in the current continuity equations) from the time-independent part \mathbf{F} . At dc bias conditions, equation (20) is reduced to:

$$\mathbf{F}(\mathbf{X}_0, \mathbf{D}_0, V_0) = 0. \quad (21)$$

Given some doping distribution \mathbf{D}_0 and bias voltage V_0 , equation (21) can be solved for state vector \mathbf{X}_0 .

3.2.2 Finding the value of the admittance

The second step is to find the values of the admittance. It is customary to assume that a sinusoidal voltage of infinitesimal amplitude v is superimposed over the dc value V_0 :

$$V = V_0 + v e^{j\omega t}. \quad (22)$$

This will induce ac perturbations in the state variable:

$$\mathbf{X} = \mathbf{X}_0 + \mathbf{x} e^{j\omega t}, \quad (23)$$

The governing equations for the ac component of the state variable \mathbf{x} can be found by linearizing equation (20) around the dc bias values \mathbf{X}_0 and V_0 :

$$\left(j\omega \hat{\mathbf{J}}_X + \hat{\mathbf{F}}_X \right) \mathbf{x} + \mathbf{F}_V v = 0, \quad (24)$$

In this equation, $\hat{\mathbf{J}}_X$ and $\hat{\mathbf{F}}_X$ are the Jacobian matrices of \mathbf{J} and \mathbf{F} computed with respect to variable \mathbf{X} (and evaluated at the dc bias point), while $\mathbf{F}_V = \frac{\partial \mathbf{F}}{\partial V}$. In general, $\hat{\mathbf{J}}_X$ and $\hat{\mathbf{F}}_X$ are $3N \times 3N$ sparse matrices, where N is the total number of mesh points.

The current through the diode can be written as a function $I(\mathbf{X})$. In the first-order approximation, the ac component of the current is:

$$i = I_X \cdot \mathbf{x}, \quad (25)$$

The admittance can now be computed by using:

$$y = \frac{I_x \cdot x}{v} \tag{26}$$

3.2.3 Finding the variance of admittance

The random doping fluctuations D^0 induce fluctuations X_0^0 and x^0 in the dc and ac components of the state variables, respectively. By using the linearization technique, one can show that:

$$\begin{cases} \hat{F}_x X_0^0 = -\hat{F}_D D^0 \\ \hat{A}x^0 + \hat{B}X_0^0 = 0, \end{cases} \tag{27}$$

where the following matrix notations have been adopted:

$$\hat{A} = j\omega \hat{J}_x + \hat{F}_x, \tag{28}$$

$$\hat{B} = \sum_{i=1}^{3N} (j\omega \hat{J}_{x,x_i} + \hat{F}_{x,x_i}) x_i. \tag{29}$$

Here: $\hat{J}_{x,x_i} = \frac{\partial \hat{J}_x}{\partial X_i}$, $\hat{F}_{x,x_i} = \frac{\partial \hat{F}_x}{\partial X_i}$, while X_i and x_i

denote the i th component of vectors X and x , respectively. Both matrices \hat{A} and \hat{B} are computed at the dc bias point (X_0, D_0, V_0) .

Once system (27) is solved for x^0 , we can find the fluctuations of admittance by using the formula:

$$y^0 = \frac{I_x \cdot x^0 + I_{xx} X_0^0 \cdot x}{v}, \tag{30}$$

where I_{xx} is the Hessian matrix of the current through the diode. The last equation is linear with respect to X_0^0 and x^0 while, according to equations (27), X_0^0 and x^0 are linear with respect to D^0 . This means that y^0 can also be evaluated as a linear combination of the fluctuations of doping at different mesh-points:

$$y^0 = \sum_{i=1}^N \gamma_y^i D_i^0, \tag{31}$$

where γ_y^i are the superposition coefficients of y .

By rearranging the terms in equations (27)-(29) it can be shown that the superposition coefficients can be compute by using the following equation:

$$\gamma_y^i = f \cdot (\hat{F}_D \delta^{(i)}), \tag{32}$$

where, by definition, $\delta^{(i)}$ is a vector whose i th component is one while all other components are equal to zero and f is the solution of the following system of linear equations:

$$\begin{cases} \hat{A}^t g = I_x, \\ \hat{F}_x^t f = \hat{B}^t g - I_{xx} x. \end{cases} \tag{33}$$

The first equation in (33) must be solved for g and the second equation for f . Then, the values of the superposition coefficients can be computed by using formula (32).

The algorithm for the calculation of admittance matrix variances can be summarized as follows:

- (1) First, the nonlinear equations (21) are solved to find the dc bias conditions throughout the device.
- (2) Second, equations (24) are solved and formula (26) is used to find the values of the admittance.
- (3) Then, matrices \hat{A} and \hat{B} are constructed by using formulas (28) and (29), respectively, and equations (33) are solved for g and f .
- (4) Then, formula (32) is used to find the values of the superposition coefficients.
- (5) Finally, the variances of the admittance elements are found from equation (9).

It is worth noting that the most computationally expensive steps in the algorithm are (1) and (2), which take about 90% of the total computation time. Steps (3), (4), and (5) take about 10% of the total computation time.

4 Numerical results

The techniques presented in the previous section was numerically implemented and used to compute the fluctuations of terminal currents and admittance in p-n junction diodes induced by random doping fluctuations. We have considered a p-n silicon diode with simplified architecture. Unless otherwise mentioned the doping concentration is assumed constant and equal to $N_a = 10^{17} \text{ cm}^{-3}$ and $N_d = 10^{16} \text{ cm}^{-3}$ in the two sides of the junction, respectively. The diode extends for about $1 \mu\text{m}$ in the x -direction and the cross-sectional area of the diode is $1 \mu\text{m}^2$. As a consequence, the results presented in this section should be scaled accordingly for other dimensions than those indicated above.

In Figure 1 we present the current through the diode by using continuous line and the standard deviation of the current by using vertical bars. The values of the standard deviations are multiplied by a factor of 10 in order to make the error bars visible on the figure. In the inset of Figure 1 we have represented the standard deviation of the current divided by the current through the diode. One can observe that between 1-2% of the total current represent fluctuations.

In Figures 2 and 3 we represent the standard

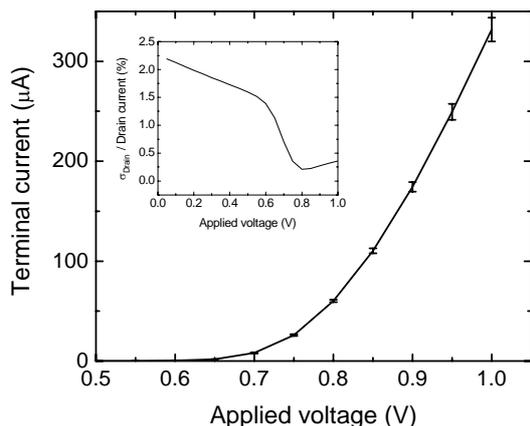


Fig. 1. Terminal current (continuous line) and standard deviation of the current (vertical bars) through the diode.

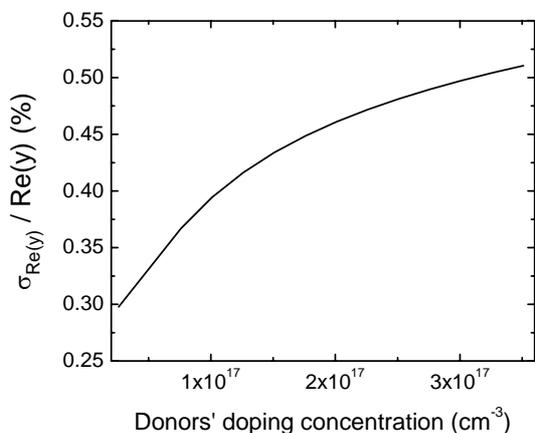


Fig. 2. Standard deviation of the real part of y-parameter divided by the real part of y-parameter.

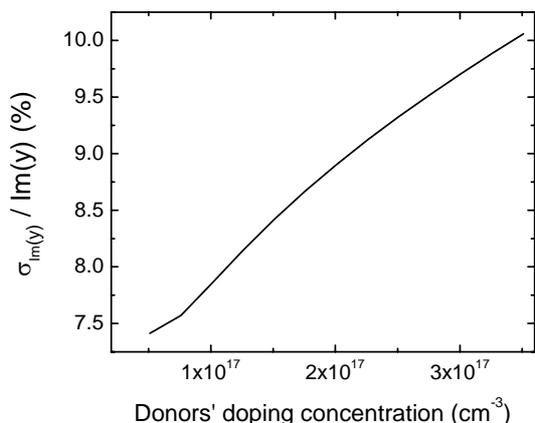


Fig. 3. Standard deviation of the imaginary part of y-parameter divided by the imaginary part of y-parameter.

deviations of the real and imaginary parts of the y-parameter divided by the value of that parameter. In both figures the doping concentration of the acceptor atoms is kept constant $N_a = 10^{16} \text{ cm}^{-3}$, while the doping concentration of the donor atoms is varied. It is important to note that the fluctuations of y-parameter increase with the doping concentration of the junction. Similar observations have been drawn for the standard deviations of threshold voltages and terminal currents in MOSFET devices [7].

5 Conclusion

Random doping induced fluctuations are becoming increasingly important in ultra small semiconductor devices, where the spatial scales of these fluctuations are comparable with the device dimensions. Significant deviations from the average values have been found for both the terminal current and the y-parameter. For a $1 \mu\text{m}^2$ diode with doping concentration of the order of 10^{16} the fluctuations represent between 1% and 10% of the average values of these parameters. However, the fluctuations increase considerable for smaller device dimensions.

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