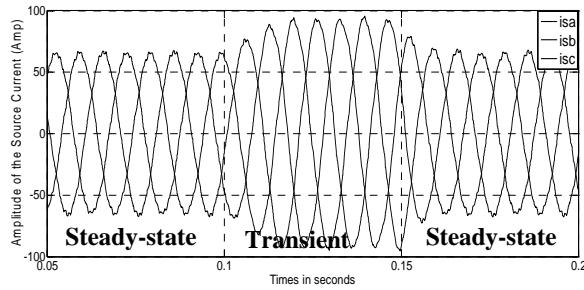


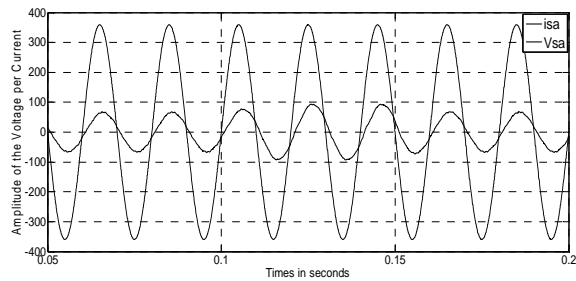
10 (d) Compensation current

Consequently current harmonics is achieved by injecting equal but opposite harmonic components at the PCC, there by cancelling the original distortion and improving the power quality on the connected power distributed system. The simulation result of source current after compensation is presented in Fig 10(e) that indicates the current is sinusoidal.



10 (e) Source current after active filter

The proposed APF system is achieved power factor correction that is shown in Fig 10(f). From the simulation we can realise a-phase voltage is in-phase with a-phase current.



10 (f) unit-power factors

The DC-bus capacitors voltage of the cascaded multilevel inverter is controlled by fuzzy logic controller. The fuzzy logic controller maintains the capacitors voltage with small ripple in steady and dynamic conditions that is shown in Fig 10(g); it serves as an energy storage element to supply a real power to operate three-phase cascaded voltage source inverter.

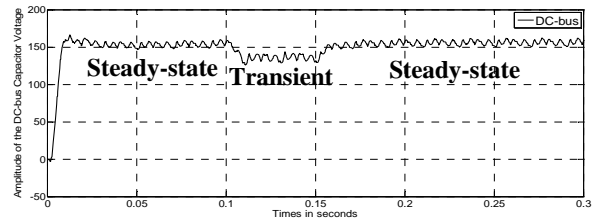


Fig 10 (g) DC-bus capacitor voltages

The Fast Fourier Transform (FFT) is used to measures the order of harmonics with the fundamental frequency 50 Hz at the source current. This order of the harmonics plotted using PI, PID and fuzzy logic-controller based cascaded active power filter systems in the supply current. The Fig 11 is plotter under FLC controller based cascaded active power system in steady state conditions.

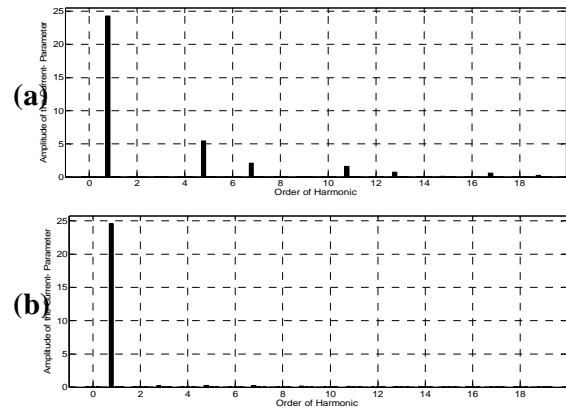


Fig 11 Order of harmonics (a) the source current without active filter (THD=25.38 %), (d)FLC based cascaded APF(THD=2.53 %)

The total harmonic distortion (THD) measured from the source current on the distribution side. The PI, PID and fuzzy logic controller based cascaded compensator filter made linear source current to the supply. The total harmonic distortion measured and compared that is presented in Table 2.

Table 2 THD measured without APF and with APF

THD conditions	Source Current(I _s) without APF	Source Current(I _s) with APF		
		PI controller	PID controller	Fuzzy logic controller
Steady state	25.38%	2.61%	2.58%	2.53%
Transient	25.32%	2.59%	2.59%	2.48%
Power factor	0.8772	0.9733	0.9721	0.9829

The simulation is done various non-linear load conditions. The PI or PID or fuzzy logic control based compensating cascaded active filter made balance responsibility even the system is non-linear load. FFT analysis of the active power filter brings the THD of the source current less than 5% into adopted with IEEE 519-1992 and IEC 61000-3 standards under non-linear load conditions.

5 Conclusions

This paper has been shown that the cascade multilevel inverter based active filter is suitable for power line conditioning in the power distribution network. The cascaded inverter provides lower costs, higher performance and higher efficiency than the traditional PWM-inverter for power line conditioning applications. A low pass filter conjunction with PI or PID or fuzzy logic control schemes has been presented for controlling the DC side capacitor voltage of the cascaded inverter and estimate the required peak reference current. The proposed PI, PID and fuzzy logic controller based APLC system is validated through extensive simulation and compared under steady state and transient condition with different non-linear loads. These simulation results reveal that the cascaded active power filter effectively filtered the current harmonics and compensated reactive volt amperes power. A comparative assessment of these three different controllers is done. The measured total harmonic distortion of the source currents is compliance with IEEE 519-1992 and IEC 61000-3 harmonic standards.

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