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Articles:

Design of 4-Bit Reversible Shift Registers
Authors: A. V. Ananthalakshmi, G. F. Sudha

High Voltage Pulse Generator by Connecting Normal Coaxial Cables in Series and in Parallel without Using Blumlein Method
Authors: Hitoshi Kijima, Koji Ochi

Slots Geometry Influence on the Air Gap Magnetic Field Distribution
Authors: Bellal Zaghdoud, Abdallah Saadoun

Based on Negotiation Strategies and Dynamic Learning Model Generation Rights Trade
Authors: Xie Chuansheng, Zhou Chengying, Li Yulong

Investigations on AQ-DBPSK Modulation based RF Transceiver for Wireless Sensor Communication Networks
Authors: Vasanthi M. S., Rama Rao T.

Testing of N-Stage 1 Bit Per Stage Pipelined ADC Using Test Input Regeneration
Authors: S. M. Hamed, A. H. Khalil, M. B. Abdelhalim, H. H. Amer, A. H. Madian

A Proposed Eleven-Transistor (11-T) CMOS SRAM Cell for Improved Read Stability and Reduced Read Power Consumption
Authors: Ajay Kumar Singh, Mah Meng Seong, C. M. R. Prabhu

A Novel and Systematic Approach to Implement Reversible Gates in Quantum Dot Cellular Automata
Authors: P. Saravanan, P. Kalpana

Epidemic Network Failures in Optical Transport Networks
Authors: Sarah Ruepp, Dimitrios Katsikas, Anna M. Fagertun

Behavioral Model of Hydrogen Bonding Network for Digital Signal Processing
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Rank-Based Ant Colony Algorithm for a Thermal Generator Maintenance Scheduling Problem
Authors: Aristidis Vlachos

A Data Adaptation Approach for a HW/SW Mixed Architecture (Case Study: 3D Application)
Authors: Tarek Frikha, Nader Ben Amor, Khaled Lahbib, Jean-Philippe Diguet, Mohamed Abid

Tracking Control of the Maximum Power Point (MPPT) In A Small Wind Turbine (SWT) For Isolated Residential Applications
Authors: David R. López Flores, José A. Pineda Gómez, Roberto Herrera S., Marcelino S. Alvarado, José A. Duarte Moller

An Off-Chip ESD Protection Strategy for High-Speed USB Interfaces
Authors: Jing-Min Wang, Chun-Ting Lin

Quadrature Hybrid Miniaturization on Single-Layer Substrate
Authors: Monthippa Uthansakul, Peerapong Uthansakul

A New Control Strategy for DFIG Wind Farm with VSC-HVDC Integration
Authors: Yong Liao, Guodong Wang

Multi-Resolution Analysis Wavelet PI Stator Resistance Estimator for Direct Torque Induction Motor Drive
Authors: Ehab H. E. Bayoumi

Preliminary Structure of Quasi Optimal Algorithm for Optimization of Analog Circuits
Authors: Alexander Zemliak, Tatiana Markina

Multi-Output Auxiliary Power Supply with Lossless Snubber
Authors: Geeng-Kwei Chang, Shu-Yuan Fan, Sheng-Yu Tseng

Analysis of Stability for Impulsive Stochastic Fuzzy Cohen-Grossberg Neural Networks with Mixed Delays
Authors: Qianhong Zhang, Jingzhong Liu, Yuanfu Shao

Assembling of the SC Circuit Matrix Based on the Status of Switches
Authors: Bohumil Brtník

Hybrid Power System with A Two-Input Power Converter
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Stabilization of DC Link Voltage Using Redundant Vectors for Five-Level Diode Clamped Shunt Active Power Filter
Authors: Thameur Abdelkrim, Karima Benamrane, Tarak Benslimane, Abdelhak Benkhelifa

A Design Approach for DC Voltage Controller of CHB-based STATCOM
Authors: Xingwu Yang

Second-Degree Polynomial Model of Laser Generation for a CuBr Laser
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A Fault Detection Method Based on Dynamic Peak-valley Limit under the Non-Steady Conditions
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JPEG2000 Hardware Implementation - Procedures and Issues
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A Method for Linearization of Optically Insulated Voltage Transducers
Authors: Daniele Gallo, Carmine Landi, Mario Luiso, Edoardo Fiorucci, Giovanni Bucci, Fabrizio Ciancetta

Explicit Model of Cylindrical Surrounding Double-Gate MOSFET
Authors: Viranjay M. Srivastava, K. S. Yadav, G. Singh

Novel Comprehensive Analytical Probabilistic Models of The Random Variations in MOSFET's High Frequency Performances
Authors: Rawid Banchuin

Applications of Artificial Neural Networks For Regulation of Temperature in a Tank
Authors: Silviya Kachulkova

Performance Analysis of Novel Domino XNOR Gate in Sub 45nm CMOS Technology
Authors: Amit Kumar Pandey, Ram Awadh Mishra, Rajendra Kumar Nagaria

Novel Powerful Comprehensive Analytical Probabilistic Model of Random Variation in Subthreshold MOSFET's Performance
Authors: Rawid Banchuin

Application of High Voltage Ratio and Low Ripple Interleaved DC-DC Converter for a Fuel Cell
Authors: Long-Yi Chang, Kuei-Hsiang Chao, Tsang-Chih Chang

A Methodology for Placement-Aware Partitioning
Authors: Wei-Kai Cheng, Tsu-Yun Hsueh, Jui-Hung Hung, Tsai-Ming Hsieh, Mely Chen Chi

Analysis of Regimes of Voltage Regulators with Limited Capacity Voltage Sources. Geometrical Approach
Authors: Penin Alexandr