

# Transport Models for Double-Gate Metal Oxide Semiconductor Field Effect Transistor Simulation

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*Abstract:* - Double-gate MOSFETs have been of great interest in these years. Simulation with drift-diffusion, hydrodynamic, Schrödinger-Poisson drift-diffusion, and density-gradient drift-diffusion models is performed to examine the model difference for double-gate MOSFETs. Different oxide thickness ( $T_{OX}$ ),  $T_{OX} = 1.5, 2, 3, 4$  nm, channel length, substrate doping concentration, and silicon film thickness are considered in this work. Device performance only can be estimated by using right transport model. In particular, there is a significant physical difference among the models for sub-100 nm double-gate MOSFETs.

*Key-Words:* - *Quantum effects, Drift-Diffusion, Hydrodynamic, Schrödinger equation, Density Gradient, Double-gate Metal Oxide Semiconductor Field Effect Transistor.*

## 1 Introduction

Double gate metal oxide semiconductor field effect transistor (DG-MOSFET) are of interest today mainly because of their inherent suppression of short-channel effects (SCEs), high transconductance and ideal subthreshold swing (S-swing) [1-6]. Thus, the scalability of semiconductor devices are intimated to nanoscale, where channel length ( $L_G$ ) is less than 10 nm. Since DG-MOSFET it expected to be the possible technology of VLSI technology, characteristics of it, such as drain current ( $I_D$ ), threshold voltage ( $V_{TH}$ ), S-swing and speed, are studies [1-6]. Computer- aided simulation for semiconductors provides a software driven approach to explore new physics of new devices. Semiconductor device models, such as Drift-Diffusion (DD) and Hydrodynamic (HD) models, which describes the variation of energy and the effects of temperature on carrier transport, are the kernal of device simulation. Both the DD and HD models are the classical description of the transport physics of devices. In order to understand the characteristics of nanoscale devices, it is important to take quantum mechanical effects into account with the classical models [7-13].

For device engineering application, a quantitatively investigation of the quantum effects, oxide thickness, channel length, doping concentration and Si-film thickness on the drain current should be addressed. In this work, DG-MOSFETs with four oxide thickness, four channel lengths, four doping concentrations and four Si-film thickness are simulated systematically so as

to discuss the variations of  $I_D$  quantitatively. Furthermore, the difference between classical and quantum mechanical models are compared by five models, which are DD, HD, Schrödinger-Poisson drift-diffusion (DD-SP), Density Gradient models (DD-DG and HD-DG) [14-16]. All simulated cases will be shown in detail later.

## 2 Modeling and Simulation

Drain current is simulated by the DD, HD, DD-SP, DD-DG and HD-DG models under different conditions. Before showing the simulated results, the models are described as the following subsections.

### 2.1 Classical Transport Models

DD and HD models are mentioned in this subsection. Firstly, the three governing equations of DD model are listed as follows. The Poisson equation is

$$\nabla \varepsilon \cdot \nabla \psi = -q(p - n + N_D - N_A), \quad (1)$$

where  $\varepsilon$  is the electrical permittivity,  $q$  is the elementary electronic charge,  $n$  and  $p$  are the electron and hole densities, and  $N_D$  and  $N_A$  are the number of ionized donors and acceptors, respectively. The other two equations are continuity equations, which are

$$q \frac{\partial n}{\partial t} - \nabla \cdot \mathbf{J}_n = -qR, \quad (2)$$

$$q \frac{\partial p}{\partial t} + \nabla \cdot \mathbf{J}_p = -qR, \quad (3)$$

where  $\mathbf{J}_n = -qn\mu_n \nabla \phi_n$  and  $\mathbf{J}_p = -qp\mu_p \nabla \phi_p$  are the electron and hole current densities.  $\mu_n$  and  $\mu_p$  are the electron and hole mobility, and  $\phi_n$  and  $\phi_p$  are the electron and hole quasi-Fermi potentials.

With continued scaling into the deep sub-micron regime, semiconductor devices can be described properly using the conventional DD model, which ignores velocity overshoot and overestimates the impact ionization generation rates. In this case, the HD model [7-8] provides a very good compromise. In the hydrodynamic model, the carrier temperatures  $T_n$  and  $T_p$  are not assumed to be equal to lattice temperature  $T_L$ , together with basic semiconductor equations, up to three additional equations can be solved to find the temperatures. In general, the model consists of the basic set of DD model and the energy conservation equations for electrons, holes and the lattice. The energy balance equations are as follows:

$$\frac{\partial W_n}{\partial t} + \nabla \cdot \mathbf{S}_n = \mathbf{J}_n \cdot \nabla E_C + \left. \frac{dW_n}{dt} \right|_{coll}, \quad (4)$$

$$\frac{\partial W_p}{\partial t} + \nabla \cdot \mathbf{S}_p = \mathbf{J}_p \cdot \nabla E_V + \left. \frac{dW_p}{dt} \right|_{coll}, \quad (5)$$

$$\frac{\partial W_L}{\partial t} + \nabla \cdot \mathbf{S}_L = \left. \frac{dW_L}{dt} \right|_{coll}, \quad (6)$$

where  $\mathbf{J}_n = \mu_n (n \nabla E_C + k_B T_n \nabla n + f_n^{td} k_B n \nabla T_n - 1.5 n k_B T_n \nabla \ln m_e)$  and  $\mathbf{J}_p = \mu_p (p \nabla E_V - k_B T_p \nabla p - f_p^{td} k_B p \nabla T_p - 1.5 p k_B T_p \nabla \ln m_h)$  are current densities.  $E_C$  and  $E_V$  are the conduction and valence band energies, respectively. The first term takes into account the contribution due to the spatial variations of electrostatic potential, electron affinity, and the band gap.  $m_e$  and  $m_h$  are the effective masses of electron and hole. The energy fluxes are  $\mathbf{S}_L = -\kappa_L \nabla T_L$ ,  $\mathbf{S}_n = -5r_n (\mathbf{J}_n k_B T_n / q + f_n^{td} \hat{\kappa}_n \nabla T_n) / 2$   $\mathbf{S}_p = -5r_p (\mathbf{J}_p k_B T_p / q + f_p^{td} \hat{\kappa}_p \nabla T_p) / 2$ , where

$$\hat{\kappa}_n = \frac{k_B^2}{q} n \mu_n T_n, \quad \hat{\kappa}_p = \frac{k_B^2}{q} p \mu_p T_p. \quad (7)$$

The collision terms are expressed by the following set of equations:

$$\left. \frac{dW_n}{dt} \right|_{coll} = -H_n - \frac{W_n - W_{n0}}{\tau_{en}}, \quad (8)$$

$$\left. \frac{dW_p}{dt} \right|_{coll} = -H_p - \frac{W_p - W_{p0}}{\tau_{ep}}, \quad (9)$$

$$\left. \frac{dW_L}{dt} \right|_{coll} = H_L + \frac{W_n - W_{n0}}{\tau_{en}} + \frac{W_p - W_{p0}}{\tau_{ep}}. \quad (10)$$

Here  $H_n, H_p, H_L$  and are the energy gain/loss terms due to generation/recombination processes. The expressions used for these terms are based on approximations made in [10]. The energy densities  $W_n = n w_n = n(3k_B T_n / 2)$   $W_p = p w_p = p(3k_B T_p / 2)$  and  $W_L = c_L T_L$ . The corresponding equilibrium energy densities are  $W_{n0} = n w_0 = n(3k_B T_L / 2)$ ,  $W_{p0} = p w_0 = p(3k_B T_L / 2)$ .

## 2.2 Quantum Mechanical Models

In principle, the Schrödinger equation coupled with classical model is the most accurate way to solve the carrier concentration, but it is not suitable for engineering applications. This is not only because it is computationally expensive but also because it is difficult to model the multi-dimensional cases. Therefore, researchers devote their efforts to develop quantum correction models, which produce a similar results to quantum mechanically calculated one but requires only about the same computation cost as that of the classical calculation. Because DG method is considered a better approximation than that of the Hänisch, van Dort model, and MLDA models [17-19], DG method is chosen in this work.

To include quantization effects in a classical device simulation, a simple approach is to introduce an additional potential, such as quantity  $\Lambda$ , in the classical density formula, which reads:

$$n = N_C \exp\left(\frac{E_F - E_C - \Lambda}{k_B T}\right), \quad (11)$$

where  $N_C$  is the conduction band density of states,  $E_C$  is the conduction band energy, and  $E_F$  is the electron Fermi energy. It is not possible to describe all quantum mechanical effects in terms of a variable  $\Lambda$ . For the SP-DD model, we include the quantization effects in the classical DD model by considering a Schrödinger equation along the semiconductor substrate ( $z$  - direction)

$$\left( -\frac{\partial}{\partial z} \frac{\hbar^2}{2m_{z,v}} \frac{\partial}{\partial z} + E_C(z) \right) \Psi_{j,v}(z) = E_{j,v} \Psi_{j,v}(z). \quad (12)$$

Together with the 2DEG formula [11, 12, 13, 17-19] the Eq. (12) is introduced to the self-consistent DD model.  $\hbar$  is the reduced Planck constant,  $E_C$  is the conduction band energy,  $v$  is the band valley,  $m_{z,v}$  is the effective mass for valley in quantization direction,  $\Psi_{j,v}$  is the  $j$ -th normalized eigenfunction in valley  $v$ ; and  $E_{j,v}$  is the  $j$ -th eigenenergy. Solving the equations above, the device current can be directly

computed. On the other hand,  $\Lambda$  is given by Eq. (13) for the DG model.

$$\Lambda = -\frac{\gamma \hbar^2 \nabla^2 \sqrt{n}}{12m \sqrt{n}}, \quad (13)$$

where  $\hbar$  is the reduced Planck constant,  $m$  is the density of states mass, and  $\gamma$  is a fitting factor.

### 3 Results and Discussion

In the numerical studies, various DG-MOSFETs characteristics, which are four oxide thickness ( $T_{OX}$ ),  $T_{OX} = 1.5, 2, 3, 4$  nm, four channel length ( $L_G$ ),  $L_G = 35, 65, 90$  and  $130$  nm, four substrate doping concentration ( $N_A$ ),  $N_A = 1 \times 10^{17}, 5 \times 10^{17}, 1 \times 10^{18}$ , and  $5 \times 10^{18} \text{ cm}^{-3}$  and four Si-film thickness ( $H$ ),  $H = 10, 20, 40, 65$  nm, are considered to simulate by classical and quantum models. Numerical results of MOSFETs are simulated by ISE-DESSIS ver. 8.0.3 [20].

Figures 2 ~ 3 illustrate the simulated results of different doping concentration by classical and quantum mechanical models. As doping concentration increases, drain current decreases. It is because a heavy doping provides more charges in the substrate. Therefore, a heavy doping also results in a large threshold voltage. The varying percentages of drain current are discussed under the turn-on condition because of large fluctuation of subthreshold current.  $\Delta I_D$  (%) is defined as  $I_D(N_A) - I_D(5 \times 10^{18} \text{ cm}^{-3}) / I_D(5 \times 10^{18} \text{ cm}^{-3})$ , where  $N_A = 1 \times 10^{17}, 5 \times 10^{17}$  and  $1 \times 10^{18} \text{ cm}^{-3}$ . According to Fig. 3, drain current varies largely with doping concentration.  $\Delta I_D$  tends toward a stable percentage when current arrives the saturated current.

Simulated results of different Si-film thickness are shown in Figs. 4 ~ 5.  $\Delta I_D$  (%) is defined as  $I_D(H) - I_D(65 \text{ nm}) / I_D(65 \text{ nm})$ , where  $H = 20, 40$  and  $65$  nm. Drain current seems not being influenced by thickness of Si-film. The results are almost the same and the percentages of difference are within 10 % in turn-on region. However, the threshold voltage varies largely.  $H$  is smaller; threshold voltage is larger because of the shielding effects. Thus, employing a thin Si-film of DG-MOSFET can retard the SCE.

Next, the influence of channel length on drain current is discussed. It is found that short channel length induce large drain current. Obviously, as channel length becomes shorter, drain current becomes larger. From Figs 6 ~ 7, the increment increases as the channel length decreases. Due to the shielding-effect performed by the double-gate, which strongly reduces drain-induced barrier lowering

(DIBL), the subthreshold current does not increase rapidly and roll-off of threshold voltage does not so sensitive to channel length scaling. To get insight into the influence of channel length,  $\Delta I_D$  (%) is defined.  $\Delta I_D = I_D(L_G) - I_D(130 \text{ nm}) / I_D(130 \text{ nm})$ , where  $L_G = 35, 65$  and  $90$  nm. Figure 7 is the comparison of different channel length in the turn-on region. Drain current is proportional to  $1/L_G$ . As channel length decreases 10 nm, a 0.1 mA/ $\mu\text{m}$  increase of drain current is observed. The increasing trend of classical and quantum are the same, however, the increasing rate of quantum cases is larger than classical cases.

The influence on oxide thickness on drain current are given in Figs. 8 ~ 9. The increase of  $I_D$  caused by oxide thickness scaling is proportional to the decrease of oxide thickness. Also, the influence is observed by  $\Delta I_D$  (%), which is defined by  $I_D(T_{OX}) - I_D(4 \text{ nm}) / I_D(4 \text{ nm})$ , where  $T_{OX} = 1.5, 2$  and  $3$  nm. A 0.5 mA/ $\mu\text{m}$  increase of drain current is observed when oxide thickness decreases 1 nm. Since oxide capacitance increases with oxide thickness scaling, threshold voltage decreases as  $T_{OX}$  decreases.

In addition, drain current simulated by different models are compared. From Fig. 10,  $I_D$  of HD is the largest and  $I_D$  of DD-DG is the smallest. That is, the temperature and energy effect enhance drain current and quantum effect reduce drain current. The result of DD-SP model is chosen as the comparing base. Comparing to HD and HD-DG models, DD, DD-DG and DD-SP models underestimated drain current. On average, a 70% ~ 60 % underestimation is observed. Therefore, the temperature and energy effect cannot be neglected. Also, the classical models overestimate drain current for 20 % ~ 30 %, especially when energy balance equations are considered. Drain currents simulated by DD, DD-DG and DD-SP are almost the same in this case. It is because the simulated channel length, which is 130 nm, and the oxide thickness, which is 4 nm, is not small enough to make quantum effect significantly. So as to understand the difference between classical and quantum effect under different physical condition, Figs. 12 ~ 13 are illustrated. The results of classical and quantum models have a 50 % difference in an extreme case. Generally, a 20 % overestimation of drain current by classical model is observed.

Although the DD model can be computed fast, it is not a good approximation of the quantum effects and the HD model. Since the difference of drain current for DD-DG and DD-SP model are insignificant, DD-DG model can be substituted for DD-SP model. The shielding effects of DG-MOSFET minimize the short channel effects and make drain current improvement with device scaling possible.

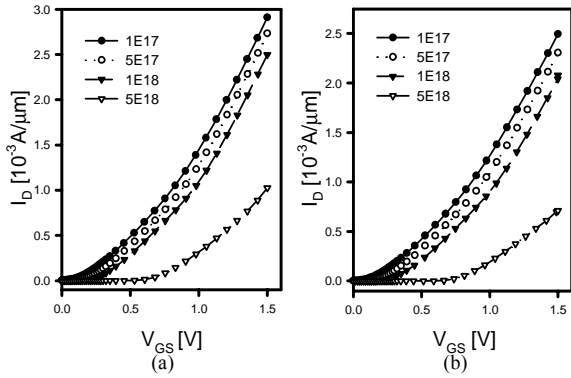


Fig. 2. Simulated  $I_D$ - $V_{GS}$  curves for different doping concentration by the (a) DD model and (b) DD-DG model.

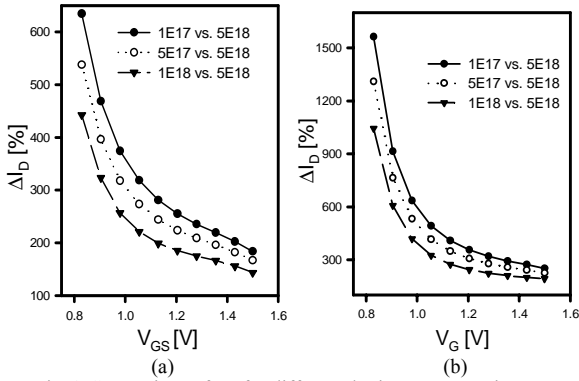


Fig. 3. Comparison of  $I_D$  for different doping concentration by the (a) DD model and (b) DD-DG model.

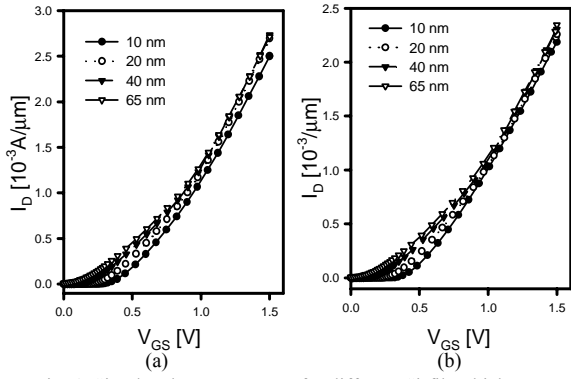


Fig. 4. Simulated  $I_D$ - $V_{GS}$  curves for different Si-film thickness by the (a) DD model and (b) DD-DG model.

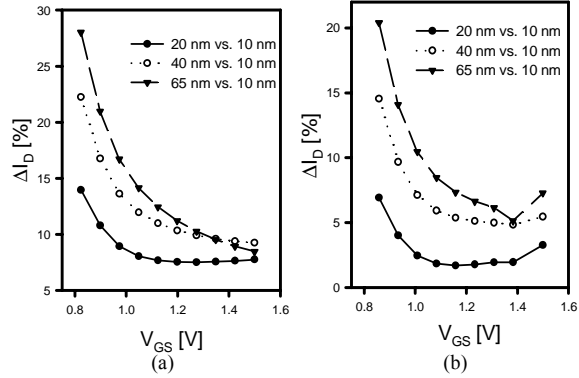


Fig. 5. Comparison of  $I_D$  for different Si-film thickness by the (a) DD model and (b) DD-DG model.

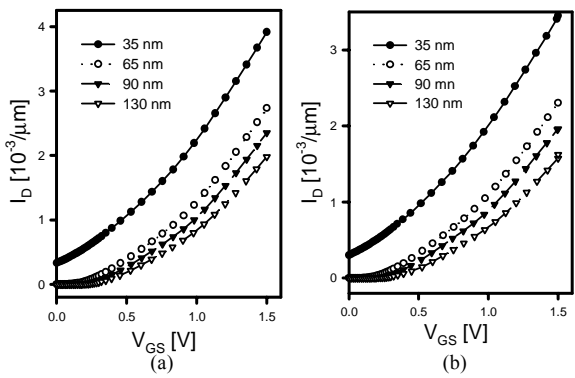


Fig. 6. Simulated  $I_D$ - $V_{GS}$  curves for different channel length by the (a) DD model and (b) DD-DG model.

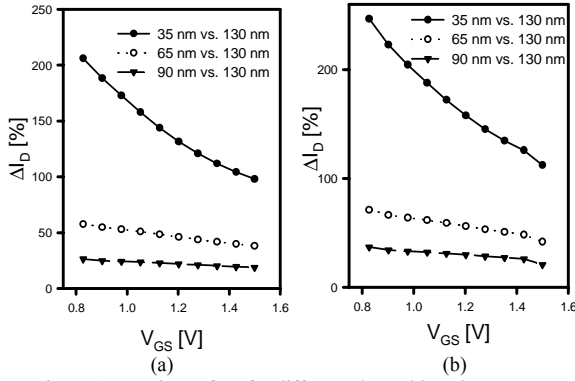


Fig. 7. Comparison of  $I_D$  for different channel length by the (a) DD model and (b) DD-DG model.

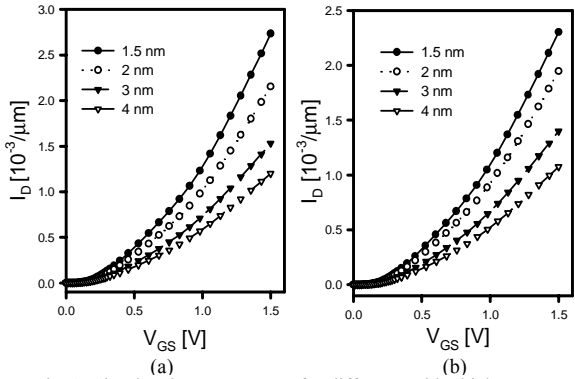


Fig. 8. Simulated  $I_D$ - $V_{GS}$  curves for different oxide thickness by the (a) DD model and (b) DD-DG model.

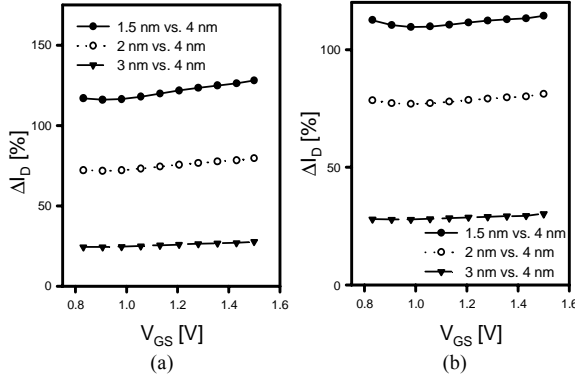


Fig. 9. Comparison of  $I_D$  for different oxide thickness by the (a) DD model and (b) DD-DG model.

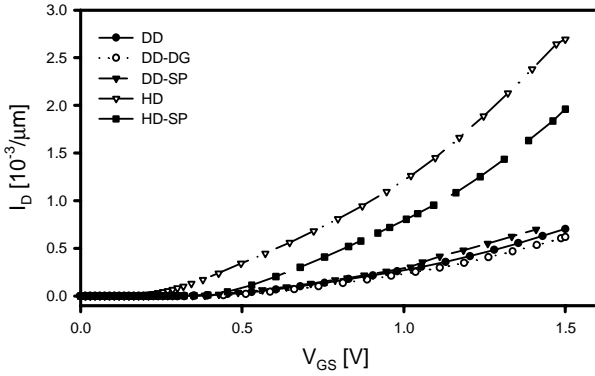


Fig. 10. Simulated  $I_D$ - $V_{GS}$  curves for different models with  $L_G = 130\text{nm}$ ,  $T_{OX} = 4\text{ nm}$ ,  $H = 65\text{ nm}$ ,  $N_D = 5E18$ .

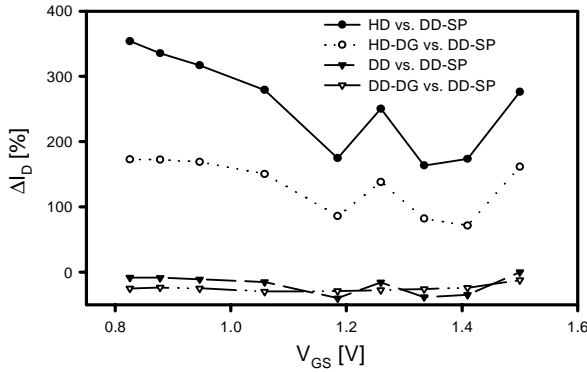


Fig. 11. Comparison of  $I_D$  for different models with  $L_G = 130\text{ nm}$ ,  $T_{OX} = 4\text{ nm}$ ,  $H = 65\text{ nm}$ ,  $N_D = 5E18$ .

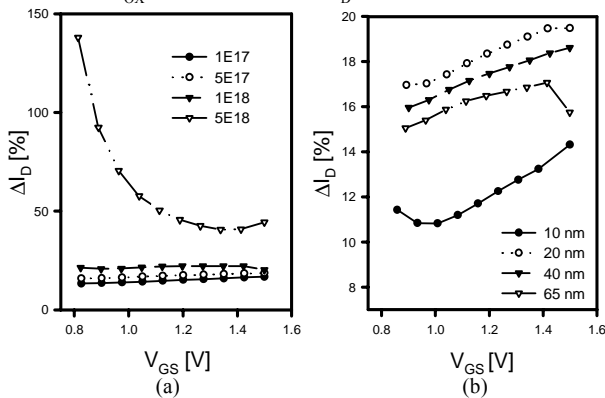


Fig. 12. Difference of  $I_D$  between classical and quantum models by (a) doping concentration and (b) Si-film thickness..

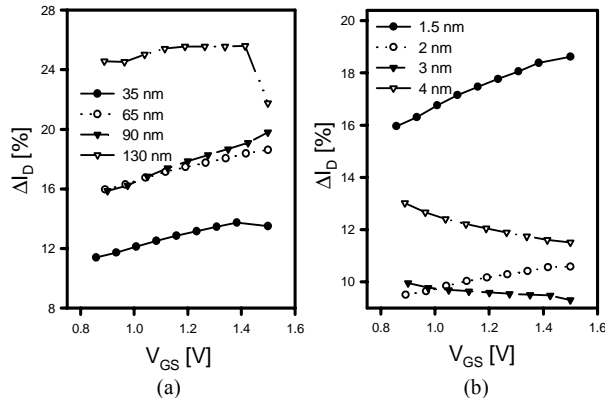


Fig. 13. Difference of  $I_D$  between classical and quantum models by (a) channel length and (b) oxide thickness..

## 4 Conclusions

In this paper, the numerical results of DD, HD, SP-DD and DG-DD models are examined so as to discuss the difference among the models quantitatively. DG-MOSFETs with four oxide thickness ( $T_{OX}$ ),  $T_{OX} = 1.5, 2, 3, 4\text{ nm}$ , four channel length, four substrate doping concentration and four Si-film thickness are simulated by ISE-DESSIS ver. 8.0.3. According to the results, quantum and energy effects must be considered in the simulation of nanoscale devices. Scaling of DG-MOSFET surely make the continuous improvement in device performance without the drawback of SCE. However, there are still transport phenomena of DG-MOSFET to be analyzed, such as formation of inversion layer and electron density distribution. To understand DG-MOSFET well, 3-D simulation, which is left to future study, is necessary.

## 5 Acknowledgement

The work was partially supported by the National Science Council, Taiwan, R.O.C., under Contracts NSC – 92 – 2215 – E – 429 - 010, NSC – 92 – 2112 – M – 429 - 001, and NSC – 92 – 2815 – C – 492 – 001 - E. It was also partially supported by Ministry of Economic Affairs, R.O.C. under contract No. 91 – EC – 17 – A – 07 - S1 - 0011.

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