

# Comparative Analysis of Double Drift Region and Double Avalanche Region IMPATT Diodes

ALEXANDER ZEMLIAK, ROQUE DE LA CRUZ  
 Department of Physics and Mathematics  
 Puebla Autonomous University  
 Av. San Claudio y 18 Sur, Puebla, 72570  
 MEXICO

*Abstract:* - The comparative analysis of the well known DDR (Double Drift Region) IMPATT diode structure and the new  $n^+p\nu p^+$  structure for the avalanche diode has been realized on the basis of the precise numerical model. The new type of the diode was named as the Double Avalanche Region (DAR) IMPATT diode. This structure includes the two avalanche regions inside the diode. The phase delay, which was produced by means of the two avalanche zones and the drift zone  $\nu$  is sufficient for the negative resistance obtain for the wide frequency region. The numerical model that is used for the analysis of the various diode structures includes all the principal features of the physical phenomena inside the semiconductor structure. The admittance characteristics of both types of the diodes were analyzed in very wide frequency region.

*Key-Words:* - Semiconductor microwave devices, modeling and simulation, numerical methods.

## 1 Introduction

The problem of the microwave power generation of the sufficient output level is the essential problem of modern semiconductor electronics. The more powerful semiconductor device is the IMPATT diode of the different structures. The single drift region (SDR) and the double drift region (DDR) IMPATT diodes are very well known [1-3] and are used successfully for the microwave power generation in millimeter region. The simulation and optimization of its parameters [4-9] can obtain very high output power level and serves as the basis for the technology to produce the diodes with the extremely characteristics. From the beginning [10] the main idea to obtain the negative resistance was defined on the basis of the phase difference producing between RF voltage and RF current due to the delay in the avalanche build-up process and the transit time of charge carriers. The transit time delay of both types of the diodes is the essential factor of the necessary phase conditions fulfillment to obtain the negative resistance. However a diode that has two avalanche regions can produce an avalanche delay, which alone can satisfy the necessary conditions to generate the microwave power. In this case the phase delay of the drift zone becomes subsidiary. The DDR diode and the DAR diode can be defined by means of the next two structures ( $n+npp+$ ,  $n+p\nu p^+$ ) in Fig. 1 (a), (b).

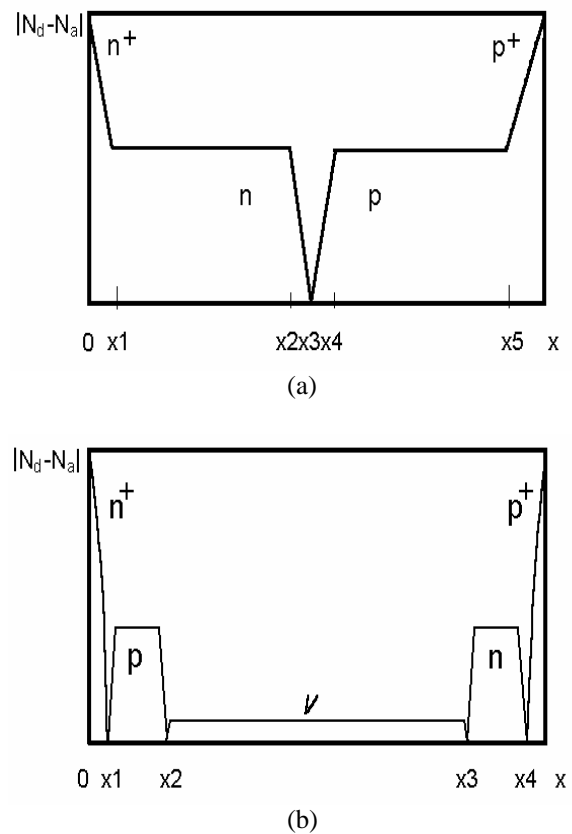


Fig. 1. Doping profile of DDR - (a) and DAR - (b) IMPATT diode.

The DAR diode has two avalanche regions around n+p and np+ junctions and one common drift region. This type of diode was suggested in [11]. The characteristics of this diode type were analyzed in DC and RF modes [12-15]. Each of the avalanche zones provides the phase delay about  $\mathbf{p}$  and this is sufficiently to produce the negative diode resistance. The electric field distribution along the axis  $x$  for both types of the diodes can be approximate by the Fig. 2.

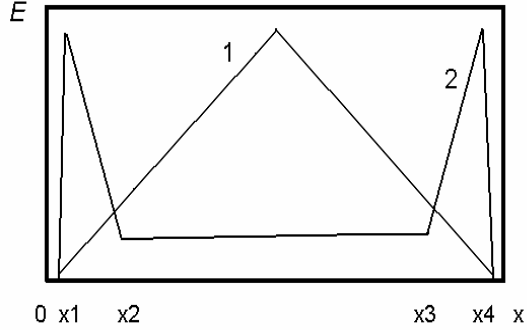


Fig. 2. Electric field distribution for DDR - 1 and DAR - 2 IMPATT diode

There is one avalanche zone for DDR diode and two avalanche zones for DAR diode. The authors of the works [11-15] affirm that the drift zone transit time delay is not a critical parameter for DAR diode because of the total avalanche delay equal to  $\mathbf{p}$ . Some advantages of the DAR structure were prognoses due this fact. The analysis provided in [11-15] gave the interesting and, at the same time very surprised results concerning the main features of the DAR diodes. Some of these results were obtained by means of the small signal model [11-13]. Others results [14-15] were obtained on the basis of simplified nonlinear model. We suppose that there is a necessity to analyze these structures on the basis of the precise model of the IMPATT diode, which was developed in some works of the authors [16-17].

## 2 Numerical Model

The electrical model which is useful for the precise analysis of the internal diode structure describes of all important physical phenomena into the semiconductor device. This model is based on the system of two continuity equations for the electrons and holes, the Poisson equation for the potential distribution in semiconductor structure and the

necessary boundary conditions as for continuity equations and for the Poisson equation.

The system of the continuity equations include two differential equations for the carriers concentrations and two additional equations for the current density determination:

$$\begin{aligned} \frac{\partial n(x,t)}{\partial t} &= \frac{\partial J_n(x,t)}{\partial x} + \mathbf{a}_n |J_n(x,t)| + \mathbf{a}_p |J_p(x,t)| \\ \frac{\partial p(x,t)}{\partial t} &= -\frac{\partial J_p(x,t)}{\partial x} + \mathbf{a}_n |J_n(x,t)| + \mathbf{a}_p |J_p(x,t)| \end{aligned} \quad (1)$$

$$J_n(x,t) = n(x,t) V_n + D_n \frac{\partial n(x,t)}{\partial x}$$

$$J_p(x,t) = p(x,t) V_p - D_p \frac{\partial p(x,t)}{\partial x}$$

where  $n, p$  are the concentrations of electrons and holes;  $J_n, J_p$  are the current densities;  $\mathbf{a}_n, \mathbf{a}_p$  are

the ionization coefficients;  $V_n, V_p$  are the drift velocities;  $D_n, D_p$  are the diffusion coefficients. Ionization coefficients, drift velocities and diffusion coefficients are the functions of two arguments; the spaces coordinate  $x$  and the times coordinate  $t$ .

The dependences of the ionization coefficients  $\mathbf{a}_n, \mathbf{a}_p$  on field and temperature have been approximated using the approach described in [18]:

$$\mathbf{a}_n(E,T) = \begin{cases} 2 \cdot 6 \cdot 10^6 e^{-[(1.4 \cdot 10^6 + 1.3 \cdot 10^3 \cdot T)/E]} & E < 2.4 \cdot 10^5 \\ 6 \cdot 2 \cdot 10^5 e^{-[(1.05 \cdot 10^6 + 1.3 \cdot 10^3 \cdot T)/E]} & 2.4 \cdot 10^5 < E < 5.3 \cdot 10^5 \\ 5 \cdot 0 \cdot 10^5 e^{-[(0.96 \cdot 10^6 + 1.3 \cdot 10^3 \cdot T)/E]} & E > 5.3 \cdot 10^5 \end{cases} \quad (2)$$

$$\mathbf{a}_p(E,T) = \begin{cases} 2 \cdot 0 \cdot 10^5 e^{-[(1.95 \cdot 10^6 + 1.1 \cdot 10^3 \cdot T)/E]} & 2 \cdot 0 \cdot 10^5 < E < 5.3 \cdot 10^5 \\ 5 \cdot 6 \cdot 10^5 e^{-[(1.296 \cdot 10^6 + 1.1 \cdot 10^3 \cdot T)/E]} & E > 5.3 \cdot 10^5 \end{cases}$$

The temperature  $T$  is expressed in  $^{\circ}C$  and the electrical field  $E$  is expressed in  $V/cm$ . The drift velocities and diffusion coefficients were calculated by means of the approximations have given in [19-21]. These approximations include all of the essential features of the carrier's mobility in silicon semiconductor structure and give the possibility to calculate the diode behavior in a wide frequency region for the different regimes.

The boundary conditions for this system include the concentration and the current definition for contact points and can be written as follows:

$$\begin{aligned} n(0,t) &= N_D(0); & p(l_0,t) &= N_A(l_0); \\ J_n(l_0,t) &= J_{ns}; & J_p(0,t) &= J_{ps}. \end{aligned} \quad (3)$$

where  $J_{ns}$ ,  $J_{ps}$  are the electron current and the hole current for inversely biased  $p-n$  junction;  $N_D(0)$ ,  $N_A(l_0)$  are the concentrations of donors and acceptors at two end space points  $x=0$  and  $x=l_0$ ; where  $l_0$  is the length of the active layer of semiconductor structure.

The electrical field distribution into semiconductor structure can be obtained from Poisson equation. As electron and hole concentrations are functions of the time, therefore, this equation is the time dependent too and time is the equation parameter. Poisson equation for the above defined problem has the following form:

$$\frac{\nabla E(x,t)}{\nabla x} = -\frac{\nabla^2 U(x,t)}{\nabla x^2} = N_D(x) - N_A(x) + p(x,t) - n(x,t) \quad (4)$$

where  $N_D(x)$ ,  $N_A(x)$  are the concentrations of donors and acceptors accordingly,  $U(x,t)$  is the potential,  $E(x,t)$  is the electrical field.

The boundary conditions for this equation are:

$$U(0,t) = 0; \quad U(l_0,t) = U_0 + \sum_{m=1}^M U_m \sin(\mathbf{w}mt + \mathbf{j}_m) \quad (5)$$

where  $U_0$  is the DC voltage on diode contacts;  $U_m$  is the amplitude of harmonic number  $m$ ;  $\mathbf{w}$  is the fundamental frequency;  $\mathbf{j}_m$  is the phase of harmonic number  $m$ ;  $M$  is quantity of harmonics.

Equations (1) - (4) adequately describe processes in the IMPATT diode in a wide frequency band. However, numerical solution of this system of equations is very difficult because of the existing a sharp dependence of equation coefficients on electric field. Evident numerical schemes have poor stability and require a lot of computing time for good calculation accuracy obtaining. It is more advantageous to use non-evident numerical scheme that has a significant property of absolute stability. Computational efficiency and numerical algorithm accuracy are improved by applying the space and the time coordinates symmetric approximation.

After the approximation of functions and its differentials, the system (1) is transformed to the non-evident modified Crank-Nicholson numerical scheme. This modification consists of two numerical systems, each of them having three-diagonal matrix. This system has the following form:

$$\begin{aligned} -(a_n - b_n) n_{i-1}^{k+1} + (1 + 2a_n) n_i^{k+1} - (a_n + b_n) n_{i+1}^{k+1} = \\ a_n n_{i-1}^k + (1 - 2a_n) n_i^k + a_n n_{i+1}^k + b_n (n_{i+1}^k - n_{i-1}^k) + \\ \mathbf{a}_n \left| \mathbf{t} \cdot V_n \cdot n_i^k + r \cdot D_n \cdot (n_{i+1}^k - n_{i-1}^k) \right| + \\ \mathbf{a}_p \left| \mathbf{t} \cdot V_p \cdot p_i^k - r \cdot D_p \cdot (p_{i+1}^k - p_{i-1}^k) \right| \end{aligned} \quad (6)$$

$$\begin{aligned} -(a_p + b_p) p_{i-1}^{k+1} + (1 + 2a_p) p_i^{k+1} - (a_p - b_p) p_{i+1}^{k+1} = \\ a_p p_{i-1}^k + (1 - 2a_p) p_i^k + a_p p_{i+1}^k - b_p (p_{i+1}^k - p_{i-1}^k) + \\ \mathbf{a}_p \left| \mathbf{t} \cdot V_p \cdot p_i^k - r \cdot D_p \cdot (p_{i+1}^k - p_{i-1}^k) \right| + \\ \mathbf{a}_n \left| \mathbf{t} \cdot V_n \cdot n_i^k + r \cdot D_n \cdot (n_{i+1}^k - n_{i-1}^k) \right| \end{aligned}$$

$$i = 1, 2, \dots, I_1 - 1; \quad k = 0, 1, 2, \dots, \infty$$

where  $a_{n,p} = \frac{\mathbf{t} D_{n,p}}{2h^2}$ ;  $b_{n,p} = \frac{\mathbf{t} V_{n,p}}{4h}$ ;  $r = \frac{\mathbf{t}}{2h}$ ;  $i$  is the space coordinate node number;  $k$  is the time coordinate node number;  $h$  is the space step;  $\mathbf{t}$  is the time step;  $I_1$  is the space node number.

The approximation of the Poisson equation is performed using the ordinary finite difference scheme at every time step  $k$ :

$$U_{i-1}^k - 2U_i^k + U_{i+1}^k = h^2 \left( N_{Di} - N_{Ai} + p_i^k - n_i^k \right) \quad (7)$$

Numerical algorithm for the calculation of IMPATT diode characteristics consists of the following stages: 1) the voltage is calculated at the diode contacts for every time step; 2) the voltage distribution is calculated at every space point from Poisson equation by factorization method [16], the electrical field distribution along the diode active layer is calculated; 3) the charge carriers ionization and drift parameters are calculated in numerical net nodes for the current time step; 4) the system of equations (6) is solved by matrix factorization method and electron and hole concentration distributions are calculated for the new time step. After this, the calculation cycle is repeated for all time steps until the end of the time period. This process is continued from one period to another until the convergence is achieved by means of the results comparison for two neighbor periods. Then all harmonics of the external current; admittance for the harmonic number  $m$  and power characteristics can be found by the Fourier transformation.

### 3 Numerical Scheme Convergence

The numerical scheme (6)-(7) characteristic analysis for the different DDR IMPATT diode had been made some years ago [16]. This analysis showed a good convergence of the numerical model. The convergence was obtained during 6 – 8 periods.

The analysis of the numerical model for the new type of the doping profile (Fig.1(b)) gave an unexpected but understandable result. The numerical scheme convergence for this type of the doping profile is very slow. The necessary number of the consequent periods depends on the operating frequency and can change from 30 – 50 for the frequency region 15 – 60 GHz until 150 – 250 periods for 200 – 300 GHz. This very slow convergence is stipulated by the asynchronies movement of the electron and hole avalanches along the same transit time region  $\nu$ . It occurs owing to the different drift velocities of the carriers. This type of numerical convergence provokes a large number of the necessary periods and a large computer time.

### 4 Comparative Analysis

The doping profile structure for the DDR and DAR IMPATT diode active layer is shown in Fig. 1. The accurate analysis for both types of the diodes has been made for the different values of the  $p$ ,  $n$  and  $\nu$  region lengths and for the different donor and acceptor concentrations. The analysis showed that

for more or less significant length of the region  $\nu$  (more than 0.5 mm) the active properties of the diode are not become. The reason is the same which for the slow mechanism convergence of the numerical model. The electron and hole avalanches have the different transit velocities but they move along the same drift region  $\nu$ . It provokes the different time delay for carriers during the transit region movement. The more length of the region  $\nu$  gives the more difference in delay time and the active properties are reduced. That is why we need reduce the length of the region  $\nu$  to obtain the necessary negative admittance. This is the contrary conclusion to all results of the papers [14-15].

Another positive idea consists of the non symmetric doping profile utilization. This profile gives some compensate of the asynchronies mechanism. Take into account these considerations the non symmetric doping profile diode was analyzed in a wide frequency range. The doping level of the  $n$ -zone is equal to  $0.5 \cdot 10^{17} \text{ cm}^{-3}$ , and for the  $p$ -zone is equal to  $0.2 \cdot 10^{17} \text{ cm}^{-3}$ . The widths of two these corresponding areas are equal to 0.1 mm and 0.2 mm accordingly. The width of the each  $p$ - $n$  junction was given as 0.02 mm from the technological aspects. The length of the drift zone  $\nu$  is equal to 0.32 mm. This type of doping profile provides a concentration of electrical field within the two  $p$ - $n$  junctions.

In Fig. 3 and Fig. 4 the small signal complex admittance i.e. the conductance versus susceptance are presented for the wide frequencies range for DDR and DAR diodes and for the current density  $J_0 = 30 \text{ kA/cm}^2$ .

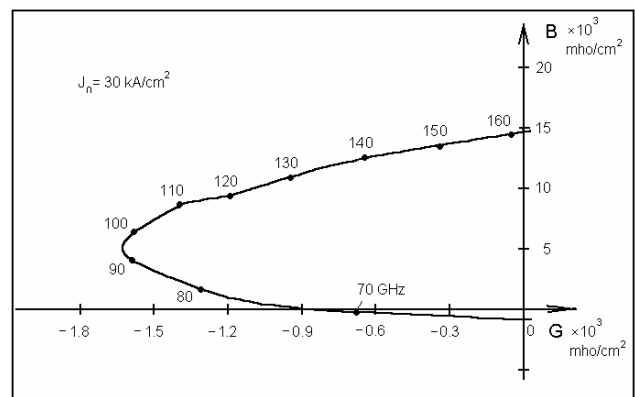


Fig. 3. DDR diode complex small signal admittance (conductance  $-G$  versus susceptance  $B$ ) plots for different frequencies.

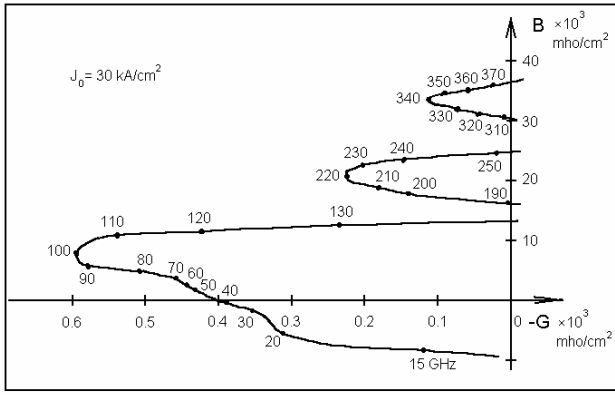


Fig. 4. DAR diode complex small signal admittance (conductance  $-G$  versus susceptance  $B$ ) plots for different frequencies.

There are some differences of the DAR diode frequency characteristics from the classical DDR IMPATT diodes. First of all the new type of the diode has three active zones in the millimetric range (Fig.4) and the DDR diode has only one zone (Fig.3). The first active zone of the DAR diode is very wide and covers the frequency region from 12 to 138 GHz. The second and the third zones give the perspective to use this structure for the high frequency generation into the millimetric range. The other difference from the usual diodes is the larger value of the real part of the complex admittance  $-G$  for two more high frequency zones. This effect permits use another frequency zones, at least the second zone, for the microwave power generation. The dependencies of the conductance  $-G$  as the function of the first harmonic amplitude  $U_1$  are shown in Fig. 5 for three frequency zones and for the same value of the current density  $J_0 = 30 \text{ kA/cm}^2$ . It is clear that the first zone characteristic ( $f = 90 \text{ GHz}$ ) has a better behavior. The maximum value of the conductance  $-G$  is a large and for the small signal achieves near the  $600 \text{ mho/cm}^2$ . The amplitude dependency for the first zone is a very soft and this provide a significant value of the generate power. Nevertheless the second and third zones (for 220 GHz and for 340 GHz) have the perspective too. These characteristics have a more sharp amplitude dependency and this effect limits the output power. However the possible optimization of the diode internal structure can improve these characteristics and permits to raise the power and the efficiency. The generate power dependencies for two frequency ranges are presented in Fig. 6 as the functions of the first harmonic amplitude. The maximum power density is equal to  $37 \text{ kW/cm}^2$  for the first frequency

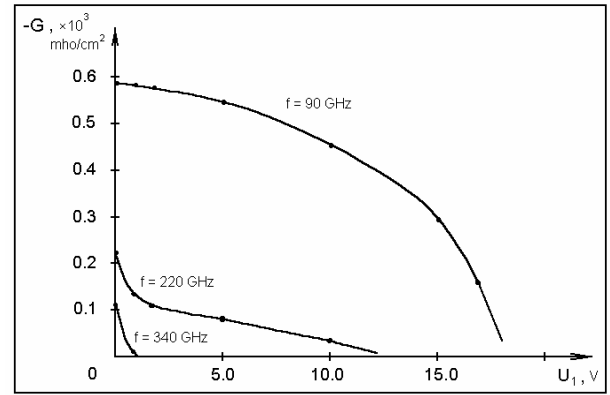


Fig. 5. Conductance  $G$  dependency as function of first harmonic amplitude  $U_1$  for three frequency zones.

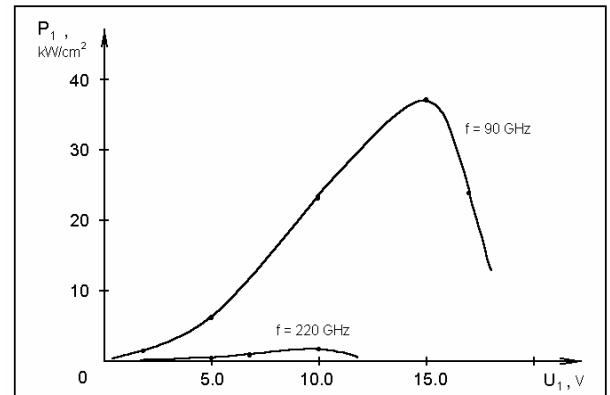


Fig. 6. Generate power  $P$  as function of first harmonic amplitude  $U_1$  for two frequency zones.

zone ( $90 \text{ GHz}$ ) and  $1.4 \text{ kW/cm}^2$  for the second one ( $220 \text{ GHz}$ ). The last result can serves as the start point for the DAR diode profile optimization to obtain the maximum output power generation for the superior frequency zones.

## 5 Conclusion

The numerical scheme that has been developed for the analysis of the different types of IMPATT diodes is suitable for the DAR complex doping profile investigation. Some new features of the DAR diode were obtained by the careful analysis on the basis of the precise numerical model. First of all we need to declare that the analysis of the DAR diode is more complicated than DDR because the very slow convergence of the numerical scheme. The principal results were obtained by the precise analysis and contradict the data that had been obtained on the

basis of the approximate models of the DAR diode. These results show that the diode do not have the active properties for the sufficiently large intrinsic region. To obtain the negative conductance we need to reduce the intrinsic region. Nevertheless the diode has a widely first frequency zone generation and the two superior frequency zones with the sufficient output power level. The DAR type of the IMPATT diode can be used for the output power generation in more widely frequency region than well known DDR diode.

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