Universal Active Current Filter Using Single
Second-Generation Current Controlled
Conveyor

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Abstract—The realization of the second-order universal (low-pass, band-pass, high-pass, band-reject, and all-pass) active current filter using a single second-generation current controlled conveyor (CCCII), two grounded capacitors, and one resistor is proposed in this paper. Three input signals and the bias current of the active element determine the filtering type of the output signals. H-Spice simulations with 0.35μm process are used to validate the theoretical predictions of the filtering signal, very low sensitivities, and the correction for the deviation of the output signals using the result of the sensitivity.

Keywords—analog circuit design, active filters, continuous-time filters, low-pass filters, band-pass filters, high-pass filters, band-reject filters, all-pass filters

I. INTRODUCTION

In 1989, it was presented that circuits based on current amplifiers operate at higher signal bandwidths, greater linearity and larger dynamic range, than their voltage-based circuit counter-parts [1]. Current-mode circuits have emerged over the last two decades as an important class of analogue circuits. A universal (low-pass, band-pass, high-pass, band-reject, and all-pass) current-mode (current input and current output) biquad which employs seven second-generation current conveyors (CCCs) in addition to two grounded capacitors and three grounded resistors was presented [2]. This biquad suffers from the following two disadvantages: requirement of one matching condition to realize an all-pass signal, and no orthogonal control of \( \omega_0 \) and \( Q \). The improved universal filter does not need any component matching conditions and enjoys the orthogonal control of \( \omega_0 \) and \( Q \) although with the same structure complexity [3]. The condensed universal filter employs only five CCIs in addition to two grounded capacitors and three grounded resistors [4] was proposed which enjoys the advantageous features: (i) no component matching, and (ii) the independent control of \( \omega_0 \) and \( Q \). The active component count of the universal filter was reduced to four but with the addition of one more grounded capacitor [5, 6], or using two floating capacitors and two floating resistors [7], or employing two grounded capacitors and two grounded resistors [8]. Then, a high output impedance multifunction filter employing three CCIs, two grounded /floating capacitors and three grounded/ floating resistors was presented [9] which cannot enjoy low active sensitivities as high quality factor is necessary. And then, a versatile multi-input-multi-output universal biquad structure was supplied using three current conveyors, two grounded capacitors, and two grounded resistors, which still has the two advantages: no orthogonal control of \( \omega_0 \) and \( Q \) and the limitation for high frequency operation [10]. The last universal filter was improved to be an insensitive one using three dual output current conveyors, two grounded capacitors, and three grounded resistors which, again, suffered from the limitation for high frequency operation [11]. The current-mode universal biquad without any matching conditions was condensed to the three-input and three-output structure with the minimum number of components, two plus type CCIs, two grounded capacitors, and two grounded resistors [12]. The single-current conveyor-based universal biquads with low quality factor [13] or high quality factor [14] were proposed using three capacitors and four/five resistors. The five generic filter (low-pass, band-pass, high-pass, band-reject, and all-pass) signals are obtained by adjusting the values of one grounded capacitor and one grounded resistor appropriately. In this paper, a new current-mode universal biquad employing a single second-generation current controlled conveyor (CCCII) [15], whose internal resistance, denoted by \( R_b \), at the input terminal \( X \) can be varied by tuning its bias current, two grounded capacitors, and one floating resistor is presented whose structure is much simpler than the previous two single-current conveyor-based universal biquads [13, 14]. Three input signals and the variable \( R_x \) determine the five different generic filtering signals obtained from the output terminal. H-Spice simulation using 0.35 μm process is used to validate the theoretical predictions of the new simple universal biquad including its five generic filtering.
output signals, very low individual component sensitivities, and the possibility of the refinement for deviation of the output signals.

II. UNIVERSAL ACTIVE CURRENT BQUAD

The universal active current filter with a single second-generation current controlled conveyor (CCCIIs), two grounded capacitors, and one resistor is shown in Fig. 1.

\[
\begin{bmatrix}
I_y \\
I_z \\
V_x
\end{bmatrix} =
\begin{bmatrix}
0 & 0 & 0 & I_x \\
-1 & 0 & 0 & V_y \\
R_x & 1 & 0 & V_z
\end{bmatrix}
\]

(1)

where \( R_x \) is the finite input resistance at the X-terminal of the CCCIIs and is given by \( V_T/2I_n \), where \( V_T \) is the thermal voltage and \( I_n \) is the bias current of the CCCIIs. The dual output signals of the CCCIIs can be easily realized by using current mirror for duplicating the output signal of the Z-terminal. The new single-current conveyor-based universal active current filter is shown in Fig. 1 using one fewer capacitor and three/four fewer resistors than the previous ones [13, 14]. Circuit analysis for Fig. 1 gives the following output current

\[
I_{out} = \frac{s^2C_1C_2 + s(C_1 + C_2)G + G_G}{s^2C_1C_2 + s(C_1 + C_2)G + G_G} - I_{in1}(G_oG) - I_{in2}(sC_1G + \alpha) - I_{in1}(sG)
\]

(2)

The resonant angular frequency and the quality factor are

\[
\omega_0 = \sqrt{\frac{\alpha G_o G}{C_1 C_2}}, \quad Q = \frac{\sqrt{\alpha \alpha C_1 C_2 G_x}}{(C_1 + C_2)G}
\]

(3)

The universal active current filter shown in Fig. 1 is then insensitive.

III. H-SPICE SIMULATIONS

The filtering performance of the new universal active current filter shown in Fig. 1 is shown in Figs. 3 to 7 by the TSMC035 level-49 H-Spice simulation (using the CMOS implementation of the CCCIIs [15], shown in Fig. 2, with the supply voltages \( V_{DD}=1.65V \), \( V_{SS}=-1.65V \), and \( W/L=5\mu m/1\mu m \) and \( 10\mu m/1\mu m \) for NMOS and PMOS transistors, respectively), and with element values \( R_T=9k\Omega \), \( G_T=222.14\mu S \) (for all-pass, \( G_T=444.28\mu S \) m), and \( C_1=50pF \), \( C_2=50pF \) for the 3dB frequency at 500kHz and \( C_2=25pF \), \( C_3=25pF \) for the 3dB frequency at 1MHz, respectively. The simulation results of the 3dB frequency of the low-pass, band-pass, high-pass, band-reject, and all-pass output signals are 544.6kHz (error 8.91%) and 1.0924MHz (error 9.24%); 524.8kHz (error 4.96%) and 1.0471MHz (error 4.71%); 498.6kHz (error -0.28%) and 0.9855MHz (error -1.45%); 524.8kHz (error 4.96%) and 1.0471MHz (error 4.71%).
716.1kHz (error 43.23%) and 1.4372MHz (error 43.72%), respectively. As we increase (resp. decrease) the capacitance value, the resonant frequency is lower (resp. higher). The low-pass, band-pass, high-pass, band-reject, and all-pass responses are shown in Figs. 8 to 12 with the frequency range from 10Hz to 302MHz (low-pass), 10Hz to 110MHz (band-pass), 10Hz to 66MHz (high-pass), 10Hz to 77.6MHz (band-reject), and 10Hz to 237MHz (all-pass). The higher the operation frequency, the larger the distortion (such as the higher the peak) due to the parasitic effect.

Fig. 2 CMOS implementation of the CCCII-

Fig. 3 Low-pass simulations at 500kHz and 1MHz

Fig. 4 Band-pass simulations at 500kHz and 1MHz

Fig. 5 High-pass simulations at 500kHz and 1MHz

Fig. 6 Band-reject simulations at 500kHz and 1MHz

Fig. 7 All-pass simulations at 500kHz and 1MHz

Fig. 8 Low-pass responses from 10Hz to 302MHz
The sensitivity frequency responses of the 3dB frequency and the peak for the low-pass and high-pass output signals are shown in Figs. 13 to 16. As can be seen, the sensitivity of the peak is very low (lower than 0.2), and only the high-pass sensitivity of the 3dB frequency is a little bit larger (the largest absolute value is 1.072).

Only tuning the values of the input resistance at the X terminal of the CCCII shown in Fig. 1 from 222.14 μm to 200.48 μm (low-pass), 200.37 μm (band-pass), 207.34 μm (high-pass), 201.45 μm (band-reject), and from 444.28 μm to 224.45 μm...
(all-pass), the deviations of the 3dB frequency are then corrected. Figs. 17 and 18 show the simulation results before and after tunings for the low-pass, band-pass, high-pass, and band-reject amplitude frequency responses, and the all-pass phase frequency response, respectively.

Fig. 17 Simulation results before and after tunings

Fig. 18 Simulation results before and after tunings

IV. CONCLUSION

The single current conveyor based universal active current-mode filter using one fewer capacitor and three/four fewer resistors than the previous ones is proposed. Due to the controllability of the finite input resistance at the X terminal of a second-generation current controlled conveyor (CCCCI) by its bias current, the five generic filtering (low-pass band-pass, high-pass, band-reject, and all-pass) signals can be obtained by proper choice of the three input current signals. TSMC 0.35 μm process H-Spice simulations are included to validate the theoretical predictions of the filtering performance component sensitivities, and the technique to correct the deviation.

REFERENCES


Chun-Ming Chang received the B.S.E.E. and M.S.E.E. degrees from National Cheng Kung University, Tainan, Taiwan, R. O. C. in 1975 and 1977, respectively, and the Ph.D. degree from the University of Southampton, Southampton, U.K., in 2004. In 1979, he joined the Department of Electrical Engineering, Taipei Institute of Technology, Taipei, Taiwan, R. O. C., as a Lecturer. After one year, he transferred to the Department of Electronic Engineering, Fu Jen Catholic University, Taipei, Taiwan, R. O. C. In 1982, he joined the Department of Electrical Engineering, Chung Yuan Christian University, Chung-Li, Taiwan, R.O.C., where he became an Associate Professor and a Full Professor in 1985 and 1991, respectively. He is currently a Professor of Electrical Engineering and leader of the Electronic Circuits Group in the Department of Electrical Engineering, Chung Yuan Christian University. He is also a departmental teacher promotion committee member and a college teacher promotion committee member. He was the chairman of the Department of Electrical Engineering of Chung Yuan Christian University from 1995 to 1999. His research interests are divided into two parts: network synthesis and analog circuit design before and after 1991, respectively. The improvement for the approach technique to factorize a paramount matrix used in network synthesis and proposed by Professor I. Cederbaum let him be promoted to a Full Professor in 1991. He has published over 60 SCI papers, in which the most famous is the invention of a new analytical synthesis method for the design of analog circuits which can, for the first time, simultaneously achieve three important criteria for the design of OTA-C filters without trade-offs. Using a succession of innovative algebra manipulation operations, a complicated nth-order transfer function can be decomposed into a set of simple equations feasible using the single-ended-input OTA and grounded capacitors. Several IEEE Transaction papers on Circuits and Systems with analytical synthesis method have been published in the literature since 2003. He is in the process of writing his professional textbook: “Analysis Circuit Design—Analytical Synthesis Method”. Prof. Chang is a senior member of the IEEE Circuits and Systems Society.