Hardware Implementation on FPGA Platform for Cellular Automata Cryptosystem

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Abstract: - This paper presents a complex project that aims the development and design of a parallel computing based cryptographic system. Cryptography is a field of major scientific and technical interest nowadays, mainly because of the increased security demands of different communication systems. The novelty and the main interest in this project is the orientation towards the hardware implementation. Special software has been developed for the analysis of different encryption/decryption strategies with cellular automata. Between the various strategies and algorithms studied, the best were selected for hardware implementation. The main issues of cellular automata hardware design are also discussed for the selected cryptosystems. The result is a VLSI project for a fast, cheap, efficient and versatile cryptographic chip.

Index Terms— cryptography, parallel computing, cellular automata, hardware/software co-design

I. INTRODUCTION

In this age of information, communications and electronic connectivity, security is a topic of general interest that should never be underestimated. The security of data bases, of data communications, of Internet connections, of scientific research and of personal e-mail and phone calls are some cases in which the encryption of data/information plays a major role. Therefore, cryptography has become an important field of theoretical research and applications development, not only in military communications as it was at its origins.

Because of its importance, cryptography is nowadays a science by itself, strongly related to other modern research fields as complexity theory, chaos, dynamical systems, computing theory etc. The state-of-the art for the field of cryptography is probably classified as it has military applications, but for the public domain a good reference can be found in [1] and [2]. The *encryption* of a message/data file/other information is a process (algorithm) that modifies this message/data file/information making it completely unintelligible, except for the person who knows the encryption *key*. The *key* refers to the encryption algorithm that has been used – in fact, to the reverse algorithm that should be used for decryption – and the particular parameters that have been used during the encryption. The *decryption* algorithm should render the original message/file/information complete and unaltered. Encryption can be achieved by constructing two different types of ciphers – stream ciphers and block ciphers. In the case of the block cipher, the message is split into successive blocks that are encrypted using a single key or multiple keys. In a stream cipher the message is broken into successive bits or characters and then the string of characters is encrypted using a key stream. The *cryptographic scheme* refers to the assembly of encryption and decryption algorithms. An ideal cryptographic scheme or algorithm has not been developed yet, as an ideal cryptographic scheme implies: no data expansion during the encoding process; fast encoding algorithm; small dimension key; fast decoding algorithm; correct and complete rendering of message after encryption/decryption; invulnerability to attacks.

The last point is a major issue in cryptography; complex mathematical studies and research have to be done in order to establish the vulnerability of each cryptographic scheme. In simple words, this answers the basic question: how difficult is to break the code? This "difficulty" has to be established in terms of complexity, cost and computing time. Therefore, depending of the particular applications, sometimes it is enough to have a code and cryptographic scheme that requires a long search for the key, although the process is very simple. This is the situation for the briefcases with cipher, where the breaking process is quite simple: one has to try *all* possible numbers in order to find the right one. Cellular automata are applied with success in cryptography mainly because their vast phenomenology and apparently big complexity require a very long computing time to break well-chosen cryptographic schemes. There are indeed a lot of parameters and factors that can drastically affect the encrypted message (cyphertext) and therefore the complexity of the attack is considerably increased. Cellular automata offer an ideal mathematical model for massive parallel computation, but most research and applications in cellular automata domain are done through simulation. However, it is obvious that only the hardware implementations of this model fully exploit its computing and high-speed possibilities. In particular, cellular automata applications in cryptography are efficient because of the massive parallelism of the model. When implemented by means of other computing systems (simulated in software or emulated with microcontrollers etc.) the parallel processes are in fact executed sequentially. Special cellular automata hardware is the only means to benefit of all the advantages of the model. This is the main reason why SCRIPT project was developed.

II. CRYPTOGRAPHY WITH CELLULAR AUTOMATA

A. The Cellular Automata Model

Basically, cellular automata are parallel systems that consist of a typically large number of finite automata (finite state machines) as elementary "cells". The cells are locally connected, in other words the global network supports only local connections. The system evolves through local changes: all cells are updated synchronously, depending on their own current state and on the states of the neighbouring cells in the network. The computing task performed by cellular automata is generally conceived as the global evolution of its configuration, starting from the initial configuration (input data) and leading to an intermediate or final configuration or attraction cycle that are interpreted as a result. This almost "visual" perceptible computation is quite an advantage in tasks like modeling and simulation, image processing and cryptography. Cellular automata are defined by the following elements: topology and dimension of the cell lattice; the neighbours set of each cell that are involved in the next state's computation; the number of states for each cell (identical for all cells; binary automata, for instance, have only two states per cell: 0/1, visually translated as white/black); the local rule that gives the next cell state. In cryptography, the main topologies that are applied are liner and two-dimensional, referring to a row or matrix of "cells".

B. Complexity of Cellular Automata

The cellular automata model is inspired from the natural model of complex systems that often consist of a large number of simple basic elements, having only local interactions that lead to a complex global behavior. This model is an important research and simulation tool in the science of complexity. Strictly speaking, the cellular automata model and its evolution are not complex, as their structure and rules are very regular and simple. But the huge dimension of the rules and configuration spaces confers to the cellular automata phenomenology a considerably great *apparent* complexity. In the quite simple example of binary linear cellular automata with 100 cells (a modest dimension) with local rules involving the central cell and two neighbors on each side (binary functions with 5 variables), there are: 2^{100} global configurations, which represent $\sim 10^{30}$ and 2^{32} possible local functions. In a rough approximation, this means around 4,000,000 functions.

Not all possible functions are of practical interest, but even in these conditions a search in the local functions' space is a very long computing task. This is related to the difficulty of cellular automata synthesis: there is no algorithm that gives the appropriate local function for a specific application. The universality of the cellular automata model is theoretically proven, but the practical applications are still waiting for development tools, since the experiment is, by now, the main means of cellular automata synthesis. The work of S. Wolfram [3] imposed a certain order in the space of local rules of functions, mainly for linear cellular automata, dividing it into four complexity classes (that express how complex is the evolution of the system governed by a certain rule). Class 3 of

Wolfram's classification contains the automata that have an apparently chaotic evolution, also strongly depending on the initial global configuration. Such an evolution is illustrated in fig. 1, where time is on the vertical axis, downwards, and the horizontal bit string is the configuration of a binary linear cellular automata.

The *class 3 automata* are ideal for applications like random sequence generation and cryptography [3],[4].
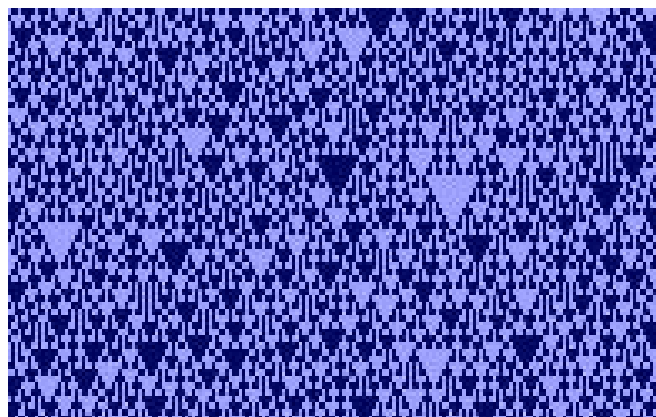

Fig. 1. Evolution of class 3 linear cellular automata

C. Applications in Cryptography

The very large phenomenology of the cellular automata model and its apparently big complexity offer a good basis for applications in cryptography (cellular automata are not the only dynamical systems applied in cryptography, and some of the basic principles of cryptography with cellular automata also stand for other dynamical systems). Massive parallelism is another feature of cellular automata that make this model attractive for cryptography, since lot of computation is often necessary in real-time applications. However, this parallelism, when emulated in software or in sequential hardware, disappears. In the last two decades, many results have been obtained in this direction. A good review of some cryptographic schemes and systems with cellular automata proposed in the scientific literature is given in [5] and [6]. In the next section some of these will be discussed as possible basis for hardware implementations, taking as valuable the theoretical results concerning the efficiency and invulnerability of the schemes.

D. Other Advantages of Cellular Automata

The design style under VLSI technology prefers the simple, modular and local connected logic circuit structure. Cellular automata are ideal from the hardware designer's point of view. The local connectivity, regularity and the simple basic components make CA very appropriate to implement low-cost and robust massive parallel machines or application-oriented circuits. The reason why there are not so many cellular-automata inspired electronic circuits is mainly the difficulty of synthesis for this computing model; however, dedicated circuits for specific applications (signal generators, associative memories, image pre-processing blocks, various simulators

for natural phenomena) have been successfully designed and produced [6].

III. SELECTION OF ALGORITHMS

A. Cryptographic Software

A special software tool was developed in order to select the cryptographic schemes that are optimal for hardware implementation. This software is necessary mainly in order to choose a (large) set of local rules as possible keys that should be integrated in hardware. The software accepts linear and two-dimensional cellular automata having variable dimensions, limited to 100/100 cells (that gives a maximum of 10^4 bits for two-dimensional encryption algorithms). In addition to the cellular automata simulation, the software completely emulates the algorithms for encryption and decryption. The software is an efficient tool for empirical analysis as well as statistic studies of all stages of the cryptographic schemes. An attractive software facility is the computation of the map of the basins of attractions, which stands only for small dimension linear cellular automata but can be qualitatively extrapolated for larger systems having the
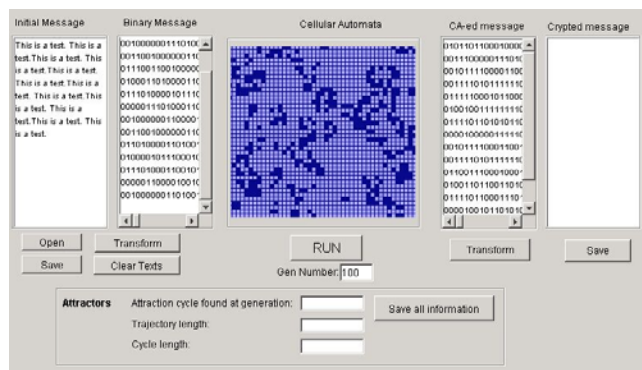

Fig. 2. The cryptographic software main window with cellular automata lattice

same rule.

B. Simulation of Cryptographic Schemes

We used a cryptosystem (typically) *linear class 3 cellular automata* as a pseudo-random generator that generates a key stream to be combined with the original message. This is split in words of given length, equal to the number of cells in the cellular automata. The key is the combination of the local rule and initial configuration. At the starting point of the encryption process, the cellular automata evolution starts. Step by step, the current configuration is a new binary word. The encryption is done through a logical combination (typically, bitwise XOR function) of each word of the original text with each configuration of the cellular automata. At each moment (time step) the encoded word of the cyphertext may be further transmitted. The decryption shall be done in the same manner, as a double application of XOR function restores the original data. Therefore, if someone knows the topology, the local rule and the initial configuration of the cellular automata, the original message can be easily obtained. The so-called "rule 30" with 3 neighbours is a typical class 3 function, but it is not the only one. The simulations have proved that the *seed*

(initial configuration) if not well-chosen may affect the behavior of the cellular automata which may be too simple and does not alter enough the plaintext. Results obtained: a library of local rules for different neighbourhoods and seeds to be transposed in hardware. This scheme is most appropriate for hardware implementation.

C. Discussion

In the field of two-dimensional cellular automata or linear cellular automata with large dimension of the neighbourhood the exhaustive analysis is not possible, for reasons stated in section II. The process is somehow random, implying the observation of the global state evolution for different rules and seeds and eventually the statistic analysis of some chosen rules. An important step of the algorithms must be emphasized here: the original message is usually divided in blocks or words that are encoded and transmitted sequentially. Therefore, the dimension of the blocks or words, which is directly related to the dimension of the cellular automata, is another parameter that can be modified in some limits, improving the invulnerability of each of the schemes.

IV. HARDWARE IMPLEMENTATION

A. The block scheme

The hardware implementation that is the final goal of the project is quite easy to design. We present here the block scheme for the key stream encryption algorithms (of type 1 and 4 in the list above). This block scheme include (Fig. 3) a central unit (CU) (that generates the configuration signals for the word dimension, the synchronization signals and the rules for the cellular automata), the cellular automata (CA) lattice that occupies most of the chip area, the block generator (BG) that divides the original data stream in blocks or words of given dimension and a logic bitwise XOR (exclusive-or)
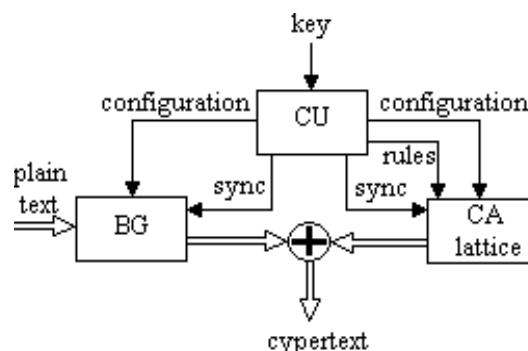

Fig. 3. The block scheme for the key stream encryption circuit.

The cellular automata lattice is a two-dimensional network of finite state machines. Even for linear topology, the cells are geometrically arranged in two-dimension and connected to obtain the linear network. The control and coordination of such a cellular automata lattice is presented in [7]. The basic cell is presented in Fig. 3, having a state register R and a programmable logic circuit. Note that the neighborhood dimension is a fundamental parameter for the hardware

implementation of the local logic circuits, as the dimension of this circuitry depends exponentially on the neighborhood dimension. Starting from this general configuration, many simple particular cryptography tasks may be implemented for different specific products, each of them having a specific set of rules to be transposed in the PLC.

B. The cryptosystem implementation on a FPGA platform

The cryptosystem was implemented on a FPGA platform Spartan III. Spartan 3 HW AFX SP 3400" (figure 4) is a very flexible evaluation platform. Spartan 3 FPGA's are programmed by loading configuration data into robust static memory cells that collectively control all function elements and routing resources. Before powering on the FPGA, configuration data is stored externally in a PROM or some other nonvolatile medium either on or off the board. In figure 2. it is presented an explicit block diagram showing all the main features of the board. This development board has 400.000 system gates which allow implementing a cellular automat with over 2500 cells (a complete encryption circuit having a cellular automat with 1024 cells require 127.048 gates). Some very important features are two digital-analog converters (DAC) 16bit 150Ksps (LTC 1865L) and two analog-digital converters (ADC) 14bit 8us conversion rate (LTC 1654). Basically there is no need for additional components on this evaluation board to create the entire encryption device and throw a PS2 socket it can be inserted on the desired signal path.

Fig. 4. Spartan 3 HW AFX SP 3400

V. CONCLUSION

Cipher schemes with cellular automata have already been studied and proved to be efficient and having good invulnerability properties. The paper presents the development and design of specific hardware, using a dedicated simulator to emulate various cryptographic schemes with different parameters. The VLSI implementation will result in cheap, robust, efficient and versatile encryption and decryption circuits that may be adapted to different devices (like phones) in order to ensure the privacy of data communication.
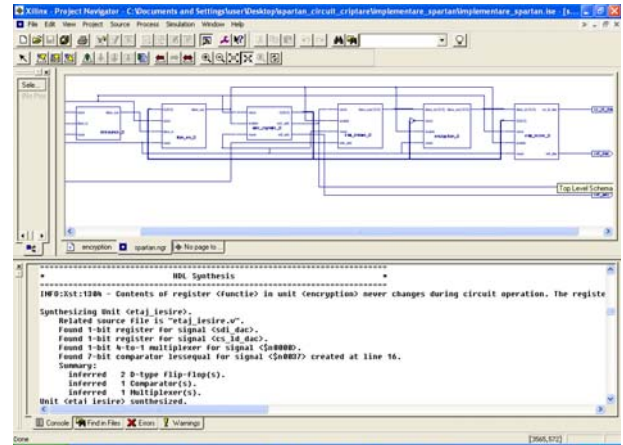
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Fig. 5. The cryptographic block scheme implemented on the FPGA platform
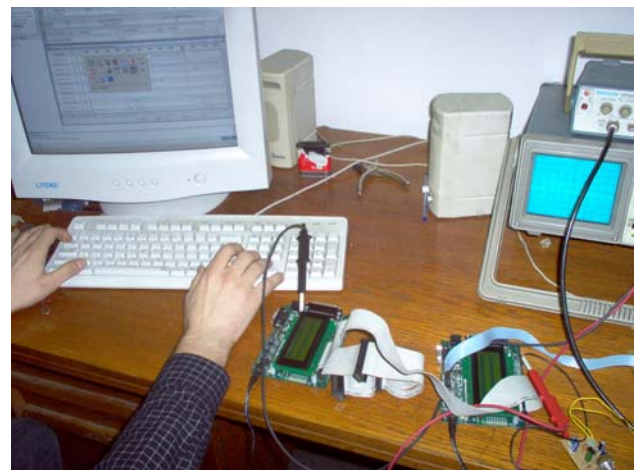

Fig. 6. Testing the cryptographic system with real data

REFERENCES

[1] William Stallings, *Cryptography and Network Security* Prentice-Hall, New Jersey, USA, 2003.
[2] Brian A. LaMacchia, *.NET Framework Security*, Addison Wesley, USA, 2002.
[3] S.Wolfram, *Cellular Automata and Complexity*, Addison-Wesley Publishing Company, 1994.
[4] J. Kari "Reversibility of 2D Cellular Automata is Undecidable", *Physica D,* Vol. 45, pp. 379-385, 1990.
[5] H.Gutowitz, *Method and Apparatus for Encryption, Decryption and Authentication using Dynamical Systems* US Patent 5365589, 1994.
[6] P. Chaudhuri, D. Chowdhury, S. Nandi and S. Chattopadhyay, *Additive Cellular Automata: Theory and Applications*, IEEE Computer Society Press, 1997.