Achieving Process Analysis in TFT-LCD Manufacturing Industry by Using Statistical Technique

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Abstract: - Enhancing yield via detailed process analysis had become an important competitiveness determinant for thin film transistor-liquid crystal displays (TFT-LCD) factories. Until now, few studies were proposed to address the related issues for process analysis in TFT-LCD industry. Therefore, the process know-how, effect analysis or the improvement chances which are hidden behind process analysis will be frequently omitted. Hence, how to apply useful techniques into analyzing the manufacturing processes will become an important issue to be addressed in TFT-LCD industry. In this study, we proposed a procedure based on statistical analysis technique to achieve the purpose. A real illustrative case owing to TFT-LCD manufacturer at Tainan Science Park in Taiwan will be applied to verifying the rationality and feasibility of our proposed procedure.

Key-Words: - process analysis, thin film transistor-liquid crystal displays (TFT-LCDs), statistical technique, stepwise regression

1 Introduction
The market for liquid crystal displays (LCDs) is known as a growing rapidly and impacting new fields. Personal digital assistants (PDAs), cellular phones, digital cameras, computers, notebooks, flat panel TVs and various computer game units will be the primary LCD products. Generally, during the past several years, the market for LCDs has grown at over 20% on average per annum. The demand gradually increased particularly in South Korea, Japan and Taiwan (Su et al., 2004). The price for LCD products is significantly reduced due to both the technology maturity and ample manufacturing capacity. The downward pricing trend further promotes LCD applications. The most widely used LCD for high information content display is the thin film transistor-liquid crystal displays (TFT-LCD). The TFT-LCD panel has a sandwich structure (Singer, 1994) consisting of two glass plates with liquid-crystal material in between. The bottom substrate is the TFT array. The top substrate is the color filter plate. Color filter glasses are usually purchased from outside vendors.

The manufacturing technology, capital investment and industrial infrastructure are key factors affecting LCD industry competition (Su et al., 2004). The ability to improve yield in the manufacturing process is an important competitiveness determinant for LCD factories due to the significant yield loss ranging from 5 to 25%. However, few studies were proposed to address the manufacturing process in detailed until now. And, it will limit or omit the possible chance of improving manufacturing process. In order to survival during the competitive environment, how to mine the useful information from the “know how” or “domain knowledge” of manufacturing process will be an important issue to all enterprises. An analysis procedure based on statistical technique to the TFT-LCD manufacturing industry was proposed in this study. In order to verify the rationality and feasibility of our approach, an illustrative example owing to TFT-LCD manufacturer at Tainan Science Park in Taiwan will be also chosen in this study.

2 Stepwise regression
Stepwise model-building techniques for regression designs with a single dependent variable had been described in numerous sources (e.g., see Darlington, 1990; Hocking, 1966, Lindeman, Merenda & Gold,
The basic procedures of Stepwise model-building will involve: (1) identifying an initial model; (2) repeatedly altering the model at the previous step by adding or removing a independent variable (or process parameters) in accordance with the "stepping criteria"; (3) terminating the search when stepping is no longer possible given the stepping criteria, or when a specified maximum number of steps has been reached. The details on the use of stepwise model-building procedures will be given as follows:

1. The initial model can be defined as Step 0. The initial model always includes the regression intercept (unless the No intercept option has been specified with respect to the real requirement). As for the backward stepwise and backward removal methods, the initial model also includes all effects specified to be included for the analysis. The initial model for these methods is therefore the whole model. For the forward stepwise and forward entry methods, the initial model only includes the regression intercept (unless the No intercept option has been specified.). The initial model may also include 1 or more effects specified to be forced into the model. If j is the number of effects specified to be forced into the model, the first j effects specified to be included are entered into the model at Step 0. Any such effects are not eligible to be removed from the model during subsequent Steps. Effects may also be specified to be forced into the model when the backward stepwise and backward removal methods are used. As in the forward stepwise and forward entry methods, any such effects are not eligible to be removed from the model during subsequent Steps.

2. The forward entry method is a simple model-building procedure. At each Step after Step 0, the entry statistic is computed for each effect eligible for entry in the model. If no effect has a value on the entry statistic which exceeds the specified critical value for model entry, then stepping is terminated, otherwise the effect with the smallest value on the removal statistic is removed from the model. Stepping is also terminated if the maximum number of steps is reached.

3. The backward removal method is also a simple model-building procedure. At each Step after Step 0, the removal statistic is computed for each effect eligible to be removed from the model. If no effect has a value on the removal statistic which is less than the critical value for removal from the model, then stepping is terminated, otherwise the effect with the smallest value on the removal statistic is removed from the model. Stepping is also terminated if the maximum number of steps is reached.

4. The forward stepwise method employs a combination of the procedures used in the forward entry and backward removal methods. At Step 1 the procedures for forward entry are performed. At any subsequent step where 2 or more effects have been selected for entry into the model, forward entry is performed if possible, and backward removal is performed if possible, until neither procedure can be performed and stepping is terminated. Stepping is also terminated if the maximum number of steps is reached.

5. The backward stepwise method employs a combination of the procedures used in the forward entry and backward removal methods. At Step 1 the procedures for backward removal are performed. At any subsequent step where 2 or more effects have been selected for entry into the model, forward entry is performed if possible, and backward removal is performed if possible, until neither procedure can be performed and stepping is terminated. Stepping is also terminated if the maximum number of steps is reached.

6. Either critical F values or critical p values can be specified to be used to control entry and removal of effects from the model. If p values are specified, the actual values used to control entry and removal of effects from the model are 1 minus the specified p values. The critical value for model entry must exceed the critical value for removal from the model. A maximum number of Steps can also be specified. If not previously terminated, stepping stops when the specified maximum number of Steps is reached.

3 Proposed Procedure

The proposed procedure will be given as follows.

Step 1. Verifying the distribution of defect count on a panel to obey a Uniform distribution or not.

Basically, a glass can be frequently divided into several panels according to the requirement of specification. Each panel will be performed via the same manufacturing processes. Hence, if there are any particular causes (e.g. the equipment operation conditions) to be happened, the defect (or abnormal position) will not have the same probability value, i.e. the distribution of defect is not a Uniform distribution.
Hence, we can make the necessary analysis to mine are there any critical areas on a panel? In order to make such analysis, we discussed with the senior engineers to initially divide a panel into a 6×6 matrix. That is, total thirty six areas are formed on a panel. Then, we can collect the defect count lying on those thirty-six areas from the historical data. According to probability philosophy, if the defect is randomly happened, the probability value of any area on a panel will obey a Uniform distribution. Restated, we can take necessary statistical test, e.g. a distribution fitness test (or $\chi^2$ test), to verify it. That is, we can make a hypothesis as follows:

- $H_0$: the defect count on a panel will obey a Uniform Distribution.
- $H_a$: the defect count on a panel can not obey a Uniform Distribution.

The statistic will have the following formula:

$$\chi^2 = \sum_{i=1}^{n} \frac{(o_i-e_i)^2}{e_i}$$  \hspace{1cm} (1)

Where, $o_i$ will denote the actual defect count on $i$-th area; $e_i$ will represent the ideal defect count obeying the Uniform distribution on $i$-th area. Then, we can take the $\chi^2$ value to compare with the cutoff value $\chi^2_{0.05}$. If the judgment is to reject $H_0$, we can obtain the conclusion as “the defect count on a panel will not significantly obey a Uniform distribution”. That is, the sensitive position (or critical area) will exist.

**Step 2.** Screening out the sensitive position (or critical area) by using stepwise regression.

Next, we can take the yield to be the output variable and the thirty-six defect counts to be input variables, and the stepwise regression technique will be performed again (The detailed procedure can be referred to Section 2). Finally, the potential critical area on a panel can be mined via stepwise regression technique. The mined information about critical area can provide the useful information about the improvement from the coefficients of the independent variables (i.e. the defect count on the critical area). For instance, if the coefficient is a negative value, it will represent the larger defect count to obtain a higher yield. Obviously, it can not obey the general recognition. Restated, it maybe denote another causes to be happened.

**4 Illustrative example**

A TFT-LCD manufacturer’s data at Tainan Science Park in Taiwan will be taken as an example to demonstrate our proposed procedure. Basically, TFT-LCD manufacturing processes can be mainly divided into three parts (in Figure 1).

![TFT-LCD manufacturing processes](image)

**Figure 1.** TFT-LCD manufacturing processes.

Then, we will take the procedure mentioned in Section 3 to perform the analysis. The detailed procedure will be given as follows:

**Step 1.** Verifying the distribution of defect count on a panel to obey a Uniform distribution or not.

According to the result obtained from Step 2, we can construct the linear yield model. Herein, yield will be viewed as the response variable (or the output variable of a system) to our proposed modeling approach, and the defect (i.e. abnormal position) count of the critical area can be taken as the input variables to our proposed procedure. That is, the relationship between yield and defect count on critical area can be represented as:

$$Yield = f(\text{defect count}_{\text{area}(1)}, \ldots, \text{defect count}_{\text{area}(36)}) \hspace{1cm} (2)$$

Where the defect count$_{\text{area}(i)}$ will denote the defect count on $(i)$-th critical area with the sequence of layer will be denoted as area $(i)$ → area $(2)$ → area $(n)$. After the linear yield model being constructed, we can obtain the useful information about the improvement from the coefficients of the independent variables (i.e. the defect count on the critical area). For instance, if the coefficient is a negative value, it will represent the larger defect count to obtain a higher yield. Obviously, it can not obey the general recognition. Restated, it maybe denote another causes to be happened.
A hypothesis will be assumed as follows:

H₀: the defect count on a panel will obey a Uniform Distribution.
Hₐ: the defect count on a panel cannot obey a Uniform Distribution.

In order to make the analysis, the thirty-six areas on a panel can be graphically depicted in Figure 2. An example of defect count on a panel will be given as Table 1(a). Then, we will process the statistical test. The ideal defect count obeying a Uniform distribution will be listed in Table 1(b), where the ideal defect count based on a Uniform distribution can be computed as (total defect count)/(total area count)=35.13889. Next, the computation procedure of statistics according to Equ. (4) will be given as Table 1(c).

Finally, we can get the statistic $X^2=583.9771$ by using Excel 2003. It significantly exceeds the cutoff value (43.77–55.76) and the judgment is made to reject H₀. That is, the distribution of defect count on a panel does not obey a Uniform distribution. Hence, we can make the detailed analysis to find out the critical area on a panel.

Step 2. Screening out the sensitive position (or critical area) by using stepwise regression.

Next, we can take the yield to be the output variable and the thirty-six defect counts to be input variables, and the stepwise regression technique will be performed. For simplifying the operation, we will employ SPSS 13.0 to perform the necessary analysis. Figure 3 will graphically depict the stepwise procedure and Figure 4 will list the result of SPSS, the final chosen model includes about eight critical area with the sequence (area 21 → area 2 → area 27 → area 30 → area 36 → area 26 → area 4 → area 6).

About 81% variation can be explained by such model and the result will be acceptable case to the real requirement. That is, it will provide more useful information to the real practice.

Step 3. Construct the yield model based on linear system and screen out the possible information about the future improvement.
According to the result obtained from Step 2, we can construct the linear yield model as follows and the comparison diagram for the actual yield and the predicted yield will be denoted in Figure 5. Besides, the correlation r equals to 0.903 and it will represent a higher correlation. It will show that the effectiveness of the fitted model.

\[
\text{Yield} = -0.974 \times \text{defect count}_{\text{area 21}} + 0.27 \times \text{defect count}_{\text{area 2}} + 0.621 \times \text{defect count}_{\text{area 27}} - 0.968 \times \text{defect count}_{\text{area 30}} + 1.225 \times \text{defect count}_{\text{area 36}} - 0.589 \times \text{defect count}_{\text{area 26}} + 0.261 \times \text{defect count}_{\text{area 4}} + 0.21 \times \text{defect count}_{\text{area 6}}
\]

(2) The yield model can be constructed based on the critical area on a panel. From the real manufacturing consideration, the constructed yield can provide more useful information for improvement.

(3) The engineers can make more detailed analysis via using the proposed approach. The yield model can be viewed as an adjustable structure and the input variables will have the chance to adjust according to their manufacturing process.

Figure 6. The comparison diagram for the actual and the predicted yield value.

From the constructed linear yield model, we can find out that several areas which do not include area 21, area 30 and area 26 will have a positive value. It will denote that the larger defect count will lead to a higher yield. After discussing with the senior engineers, we can make an assumption that the defect on those areas will represent the clustering effect from the real engineering experience or knowledge. That is, the necessary clustering analysis will be the subsequent job to make the process improvement in the future.

4 Conclusion

After applying a real example to demonstrate the proposed procedure based on statistical technique, we find out that the process analysis will make the practitioners to understand their domain knowledge or manufacturing core. The advantage of the proposed procedure can be summarized as:

(1) We analyzed the potentially sensitive position or critical area with respect to the abnormal position (or defect) on LCD-TFT panel and a complete data mining procedure is also proposed in this study. It will be viewed as a rational and feasible reference for the future analysis.

References: